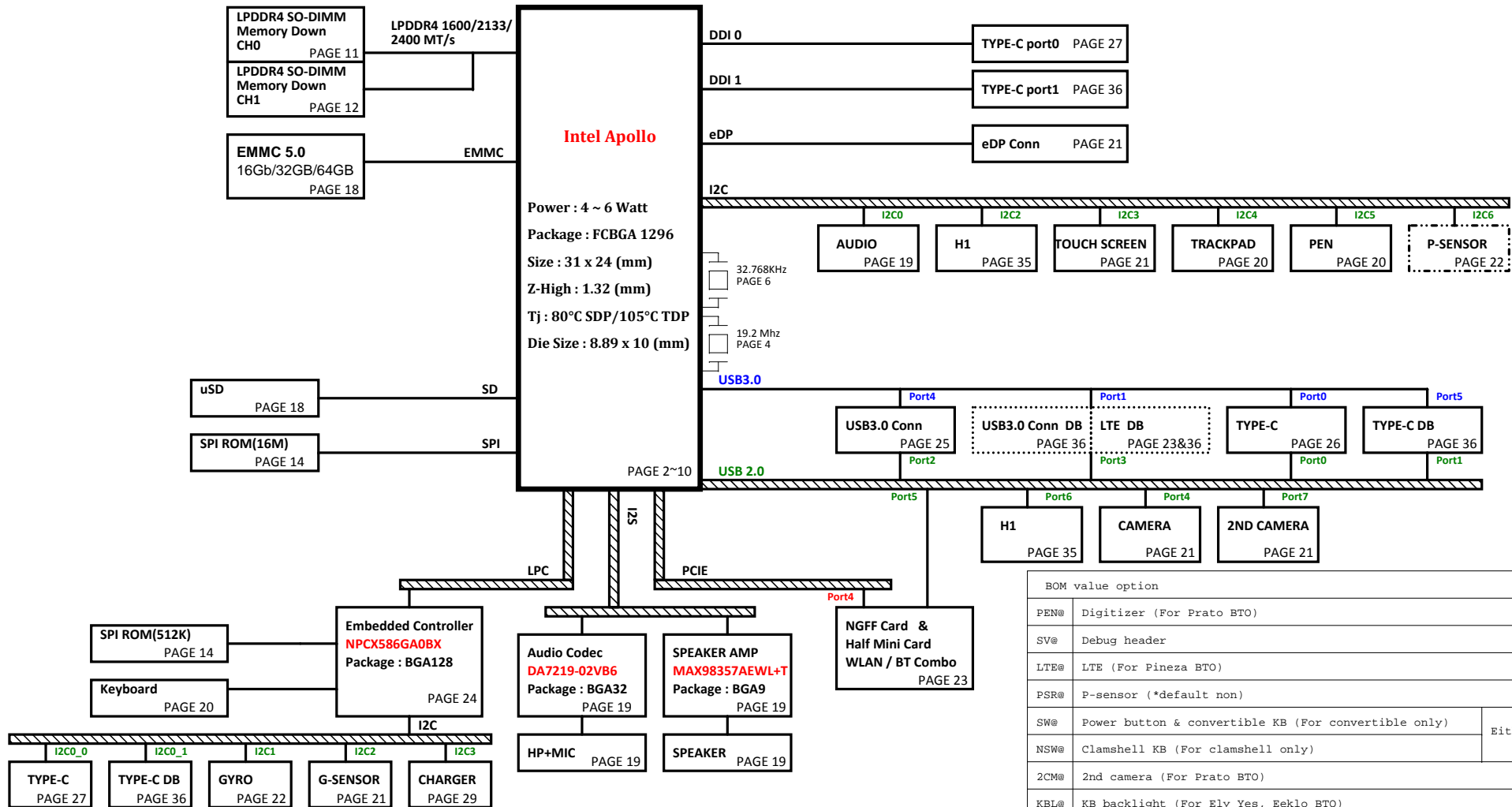


Intel Apollo Platform Block Diagram

PCB 6L STACK UP

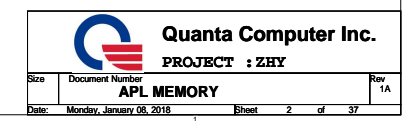
LAYER 1 : TOP
LAYER 2 : VCC
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : GND
LAYER 6 : BOT



BOM value option		
PEN@	Digitizer (For Prato BTO)	
SV@	Debug header	
LTE@	LTE (For Pineza BTO)	
PSR@	P-sensor (*default non)	
SW@	Power button & convertible KB (For convertible only)	Either one
NSW@	Clamshell KB (For clamshell only)	
2CM@	2nd camera (For Prato BTO)	
KBL@	KB backlight (For Ely Yes, Eeklo BTO)	
GS@	G-sensor (For convertible only)	
TN@	Touchscreen (For convertible Yes, clamshell BTO)	
GY@	Gyro (For convertible only)	
ZAL@	For Astronaut only (acoustic noise)	

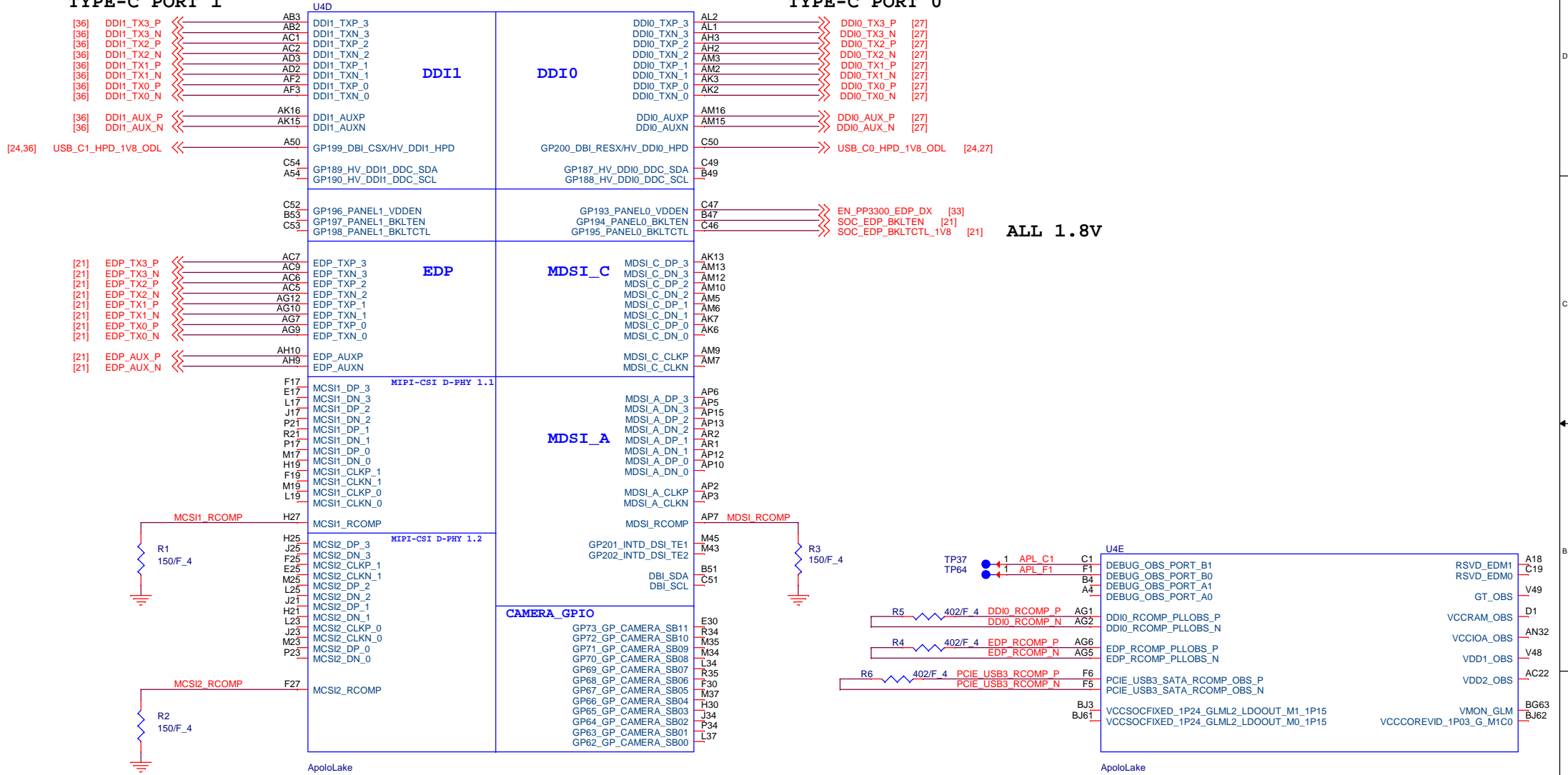
*11" and 15" USB3 TYPE-A connector and audio jack are different

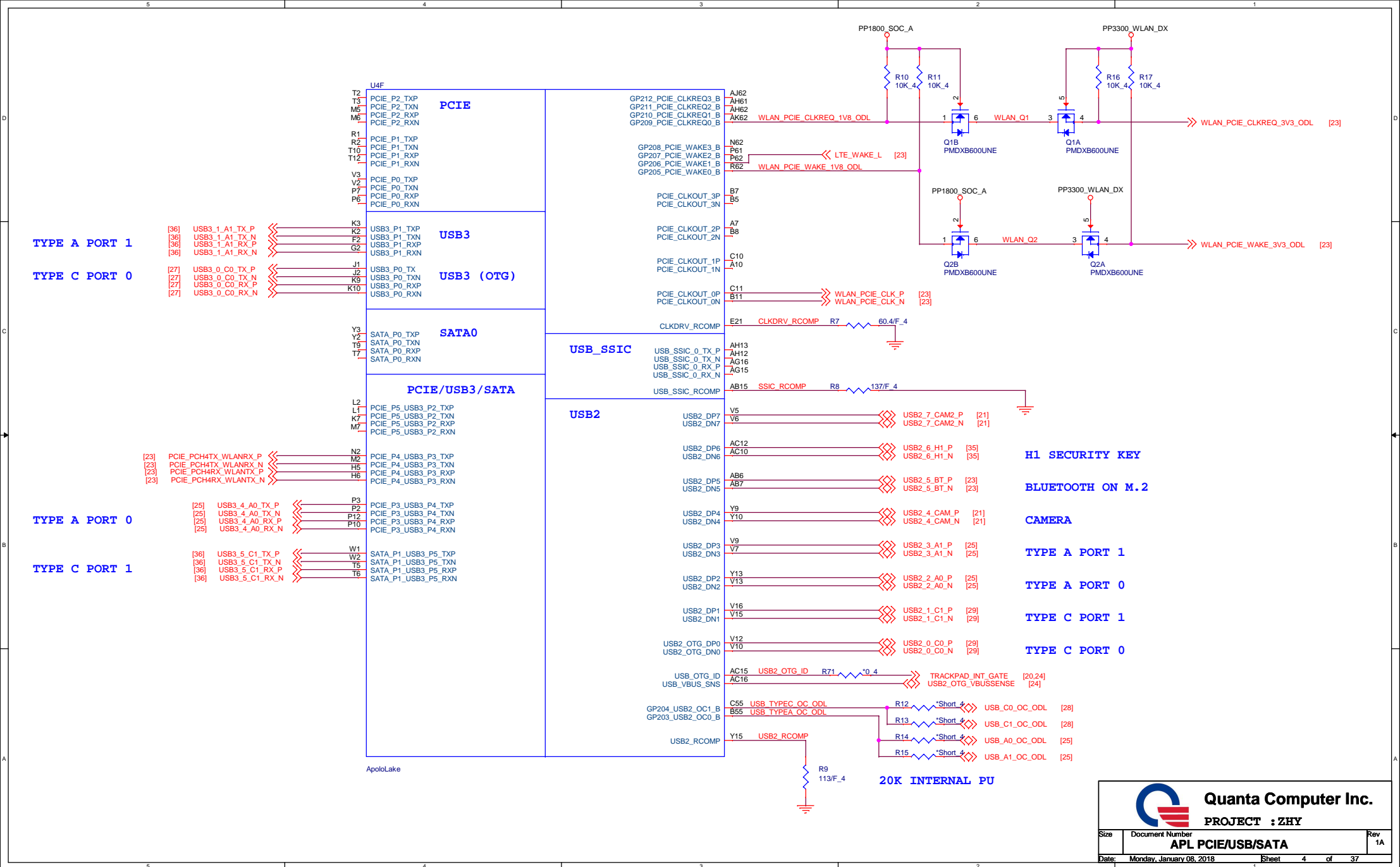
www.schematic-x.blogspot.com

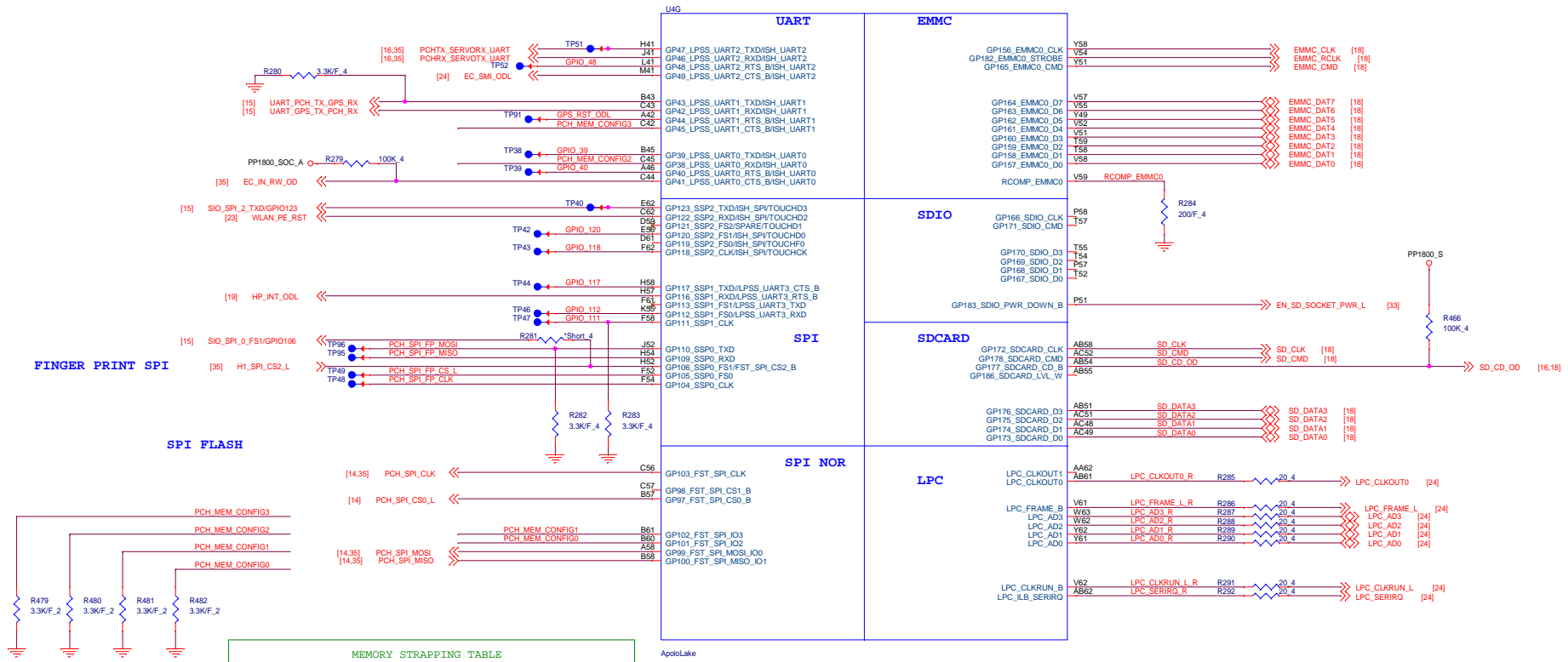


TYPE-C PORT 1

TYPE-C PORT 0

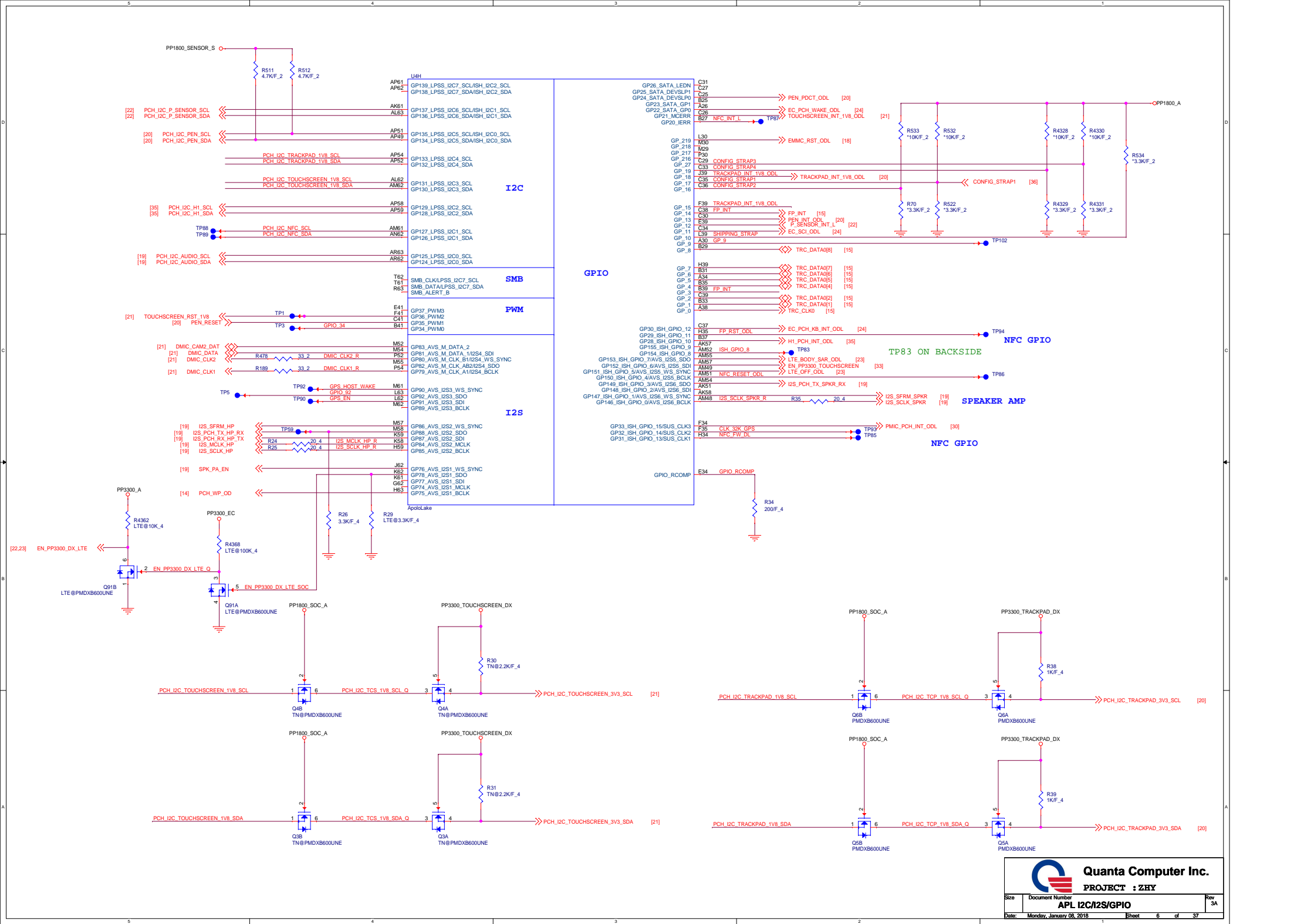


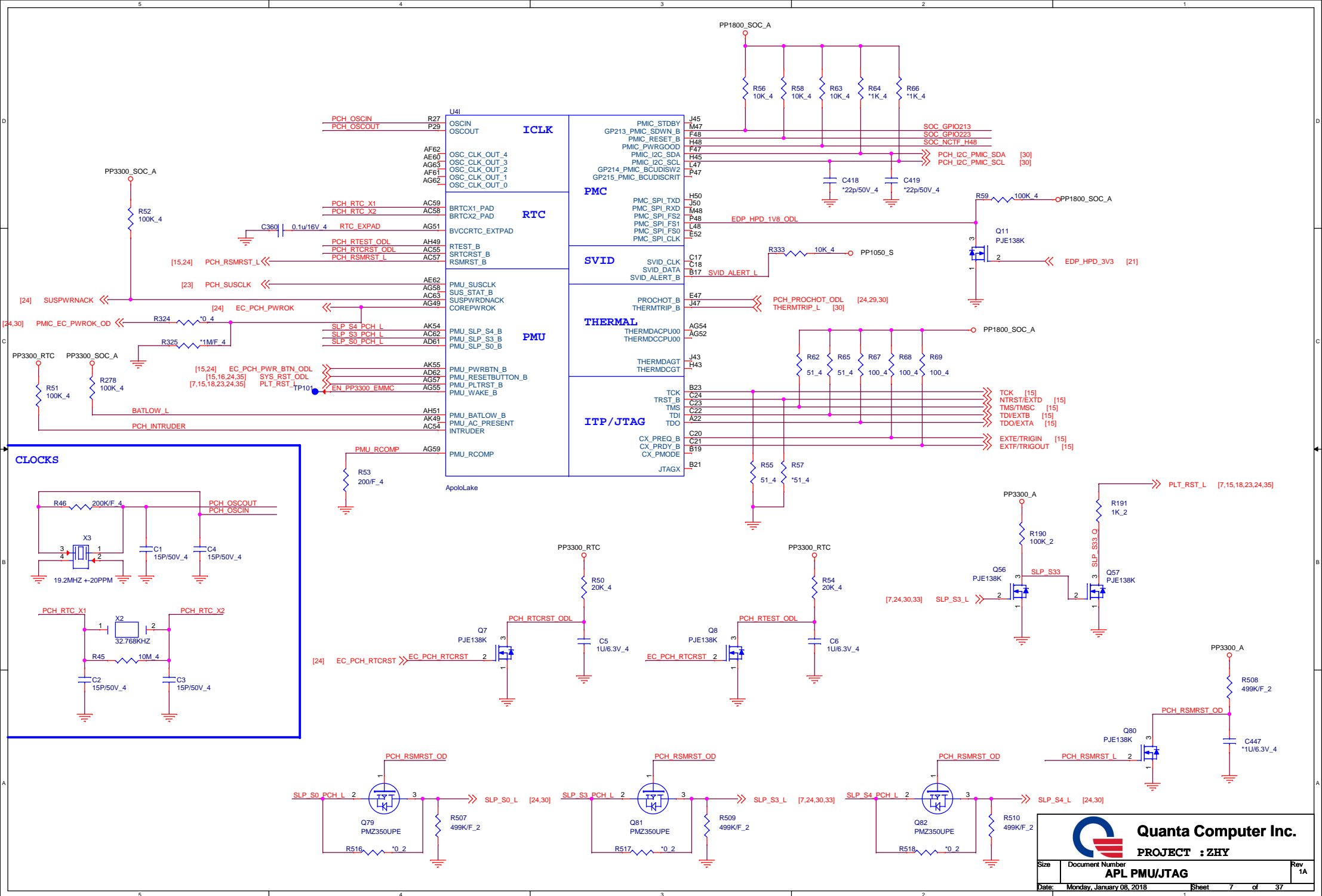


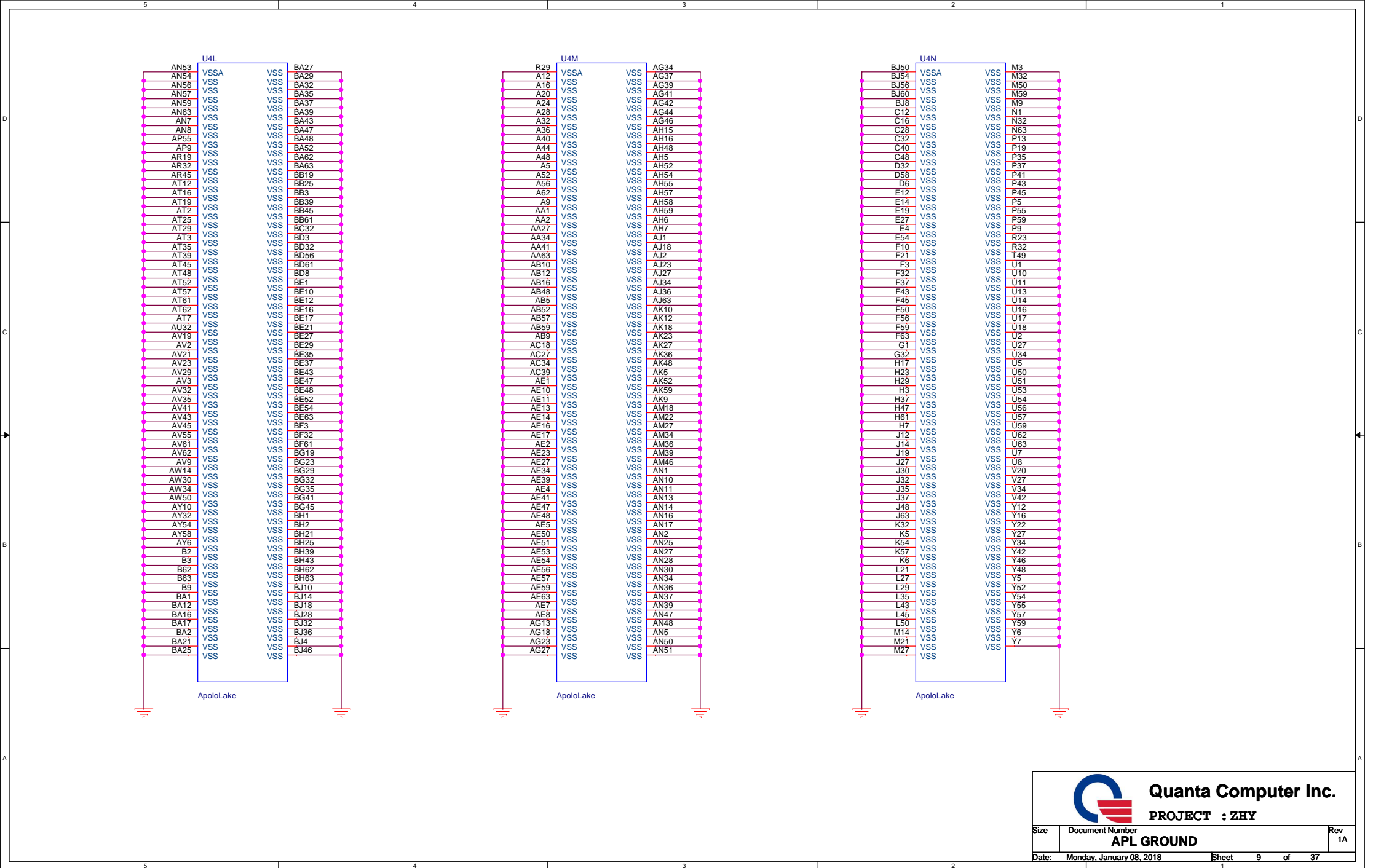


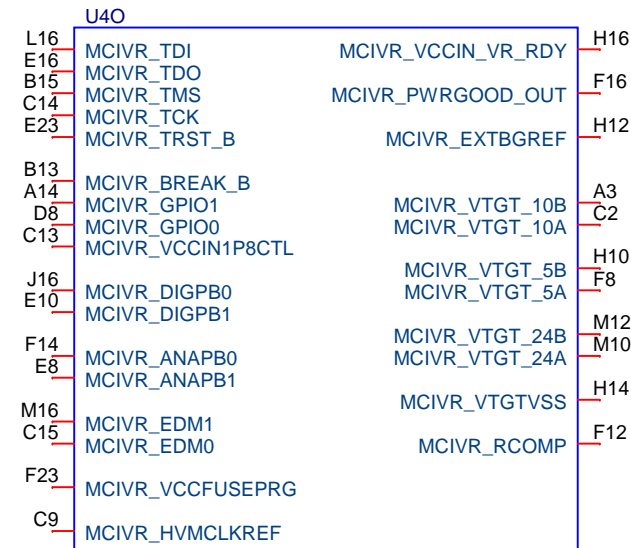
MEMORY STRAPPING TABLE				
CONFIG PIN	3	2	1	0
SAMSUNG 8GB K4F6E304HB	0	0	0	0
SAMSUNG 4GB K4F8E304HB	0	0	0	1
MICRON 8GB MT53B512M32D2NP-062WT:C	0	0	1	0
MICRON 4GB MT53B256M32D1NP-062 WT:C	0	0	1	1
HYNIX 8GB H9HCNNNBPUMLHR	0	1	0	0
HYNIX 4GB H9HCNNNBKUMLHR	0	1	0	1

HARDWARE STRAPS (* = SYSTEM STRAP SELECTION)				
GPIO_39: INTERNAL 20K PD *DISABLE CSE ROM BYPASS: 0 ENABLE CSE ROM BYPASS: 1	GPIO_47: INTERNAL 20K PD *DON'T FORCE DNX FW LOAD: 0 FORCE DNX FW LOAD: 1	GPIO_106: INTERNAL 20K PU *MUST BE HIGH WHEN RSM_RST_N DEASSERTS	GPIO_117: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	GPIO_120: INTERNAL 20K PD *TOP SWAP OVERRIDE DISABLE: 0 TOP SWAP OVERRIDE ENABLE: 1
GPIO_40: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	GPIO_48: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	GPIO_111: INTERNAL 20K PU DO NOT BOOT FROM SPI: 1 *BOOT FROM SPI: 0	GPIO_123: INTERNAL 20K PU *MUST BE HIGH WHEN RSM_RST_N DEASSERTS	GPIO_121: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS
GPIO_43: INTERNAL 20K PU ENABLE BOOT FROM EMMC: 1 *DISABLE BOOT FROM EMMC: 0	GPIO_104: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	GPIO_118: INTERNAL 20K PD *NO FLASH DESCRIPTOR OVERRIDE: 0 OVERRIDE FLASH DESCRIPTOR: 1	GPIO_112: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	
GPIO_44: INTERNAL 20K PU *ENABLE BOOT FROM SPI: 1 DISABLE BOOT FROM SPI: 0	GPIO_105: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	GPIO_110: INTERNAL 20K PU LPC BUFFERS AT 1.8V: 1 *LPC BUFFERS AT 3.3V: 0	GPIO_113: INTERNAL 20K PD *MUST BE LOW WHEN RSM_RST_N DEASSERTS	

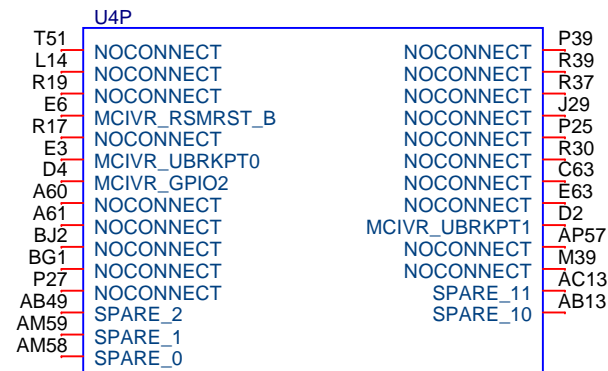








ApoloLake



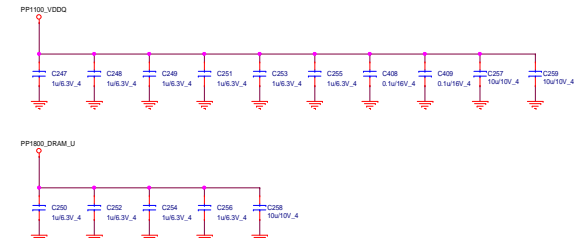
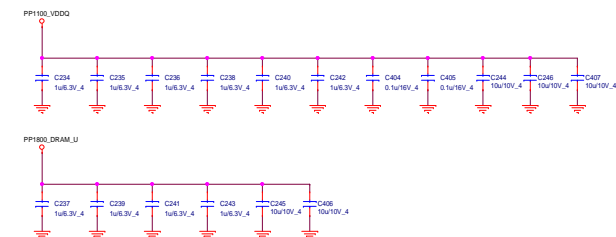
ApoloLake

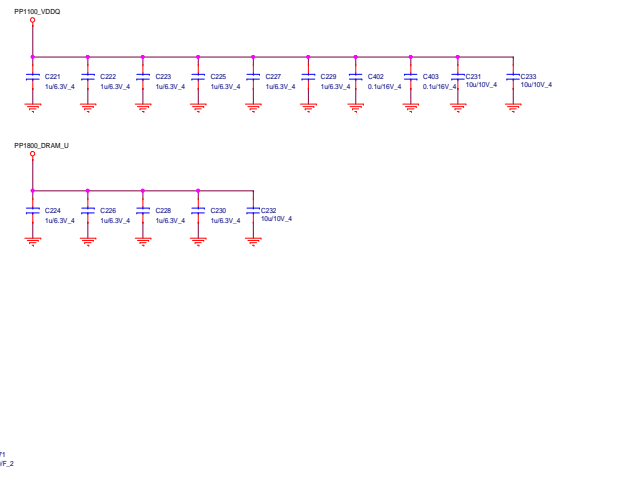
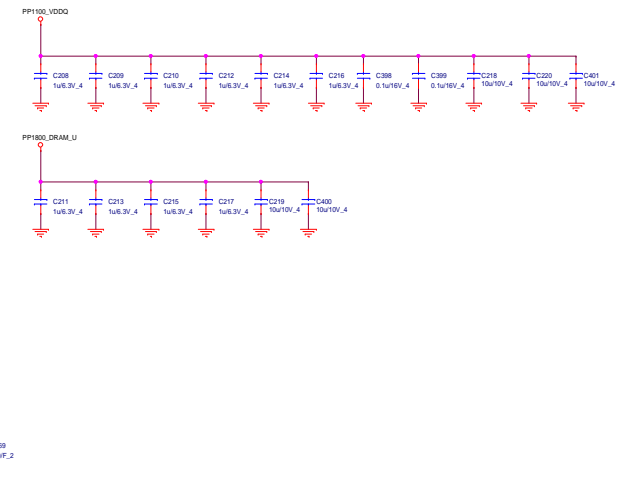


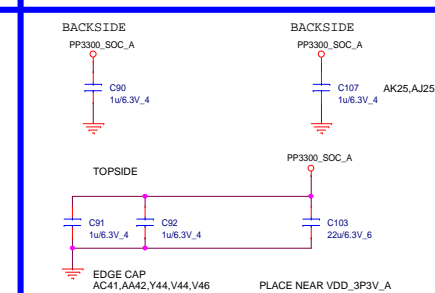
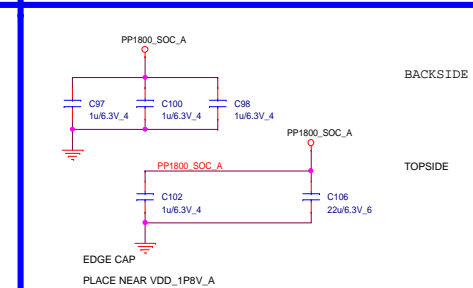
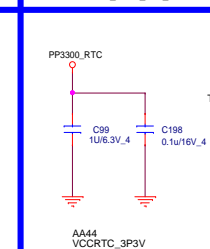
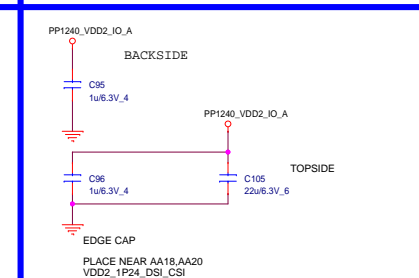
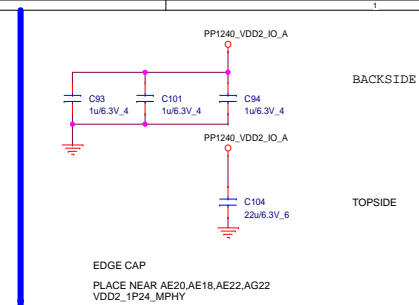
Quanta Computer Inc.

PROJECT : ZHY

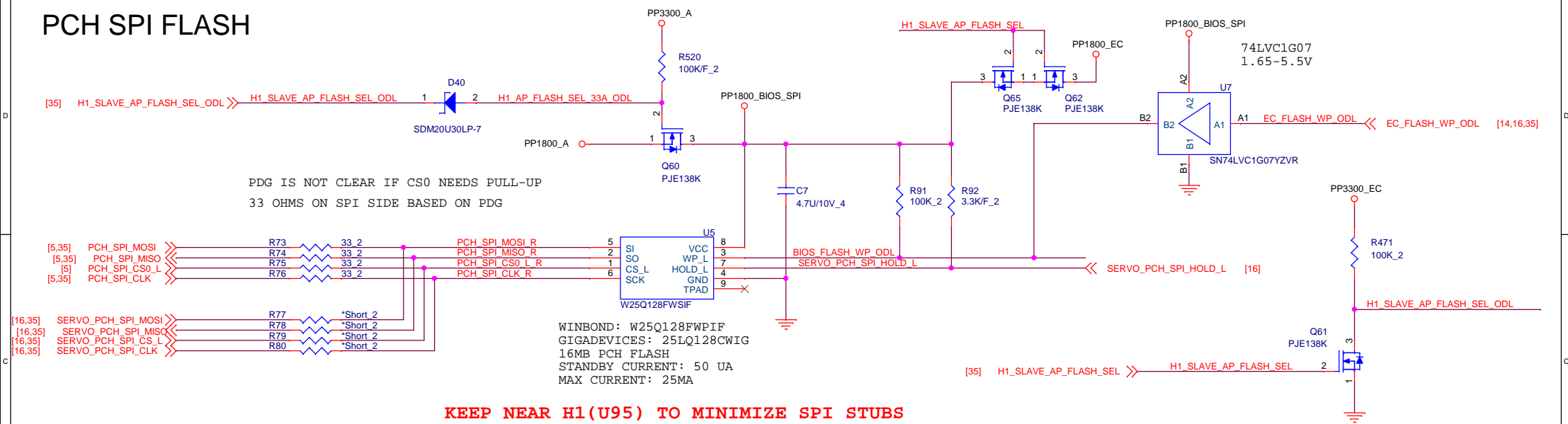
Size	Document Number	Rev
	APL NO CONNECT	1A
Date:	Monday, January 08, 2018	Sheet 10 of 37



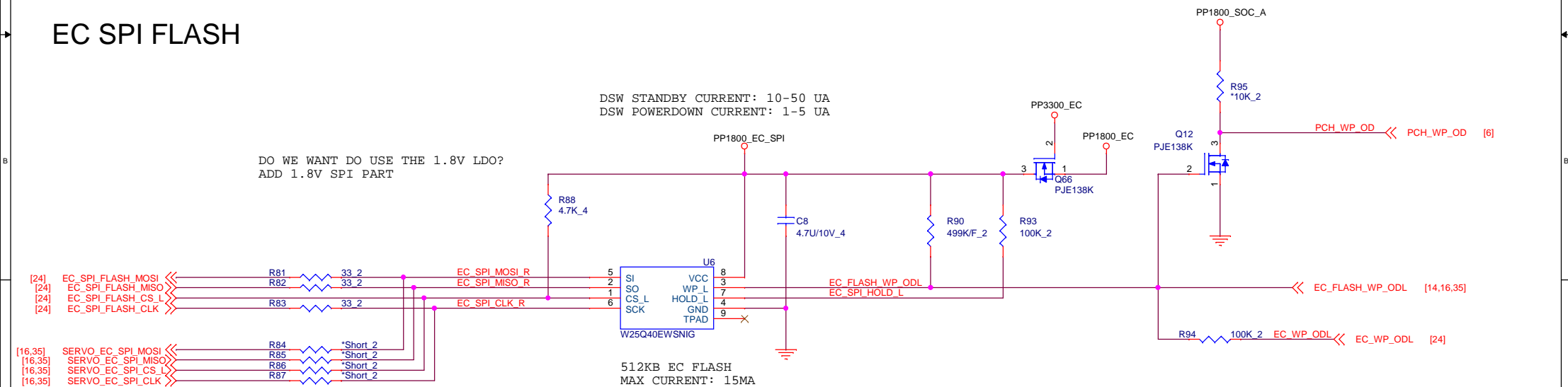




PCH SPI FLASH

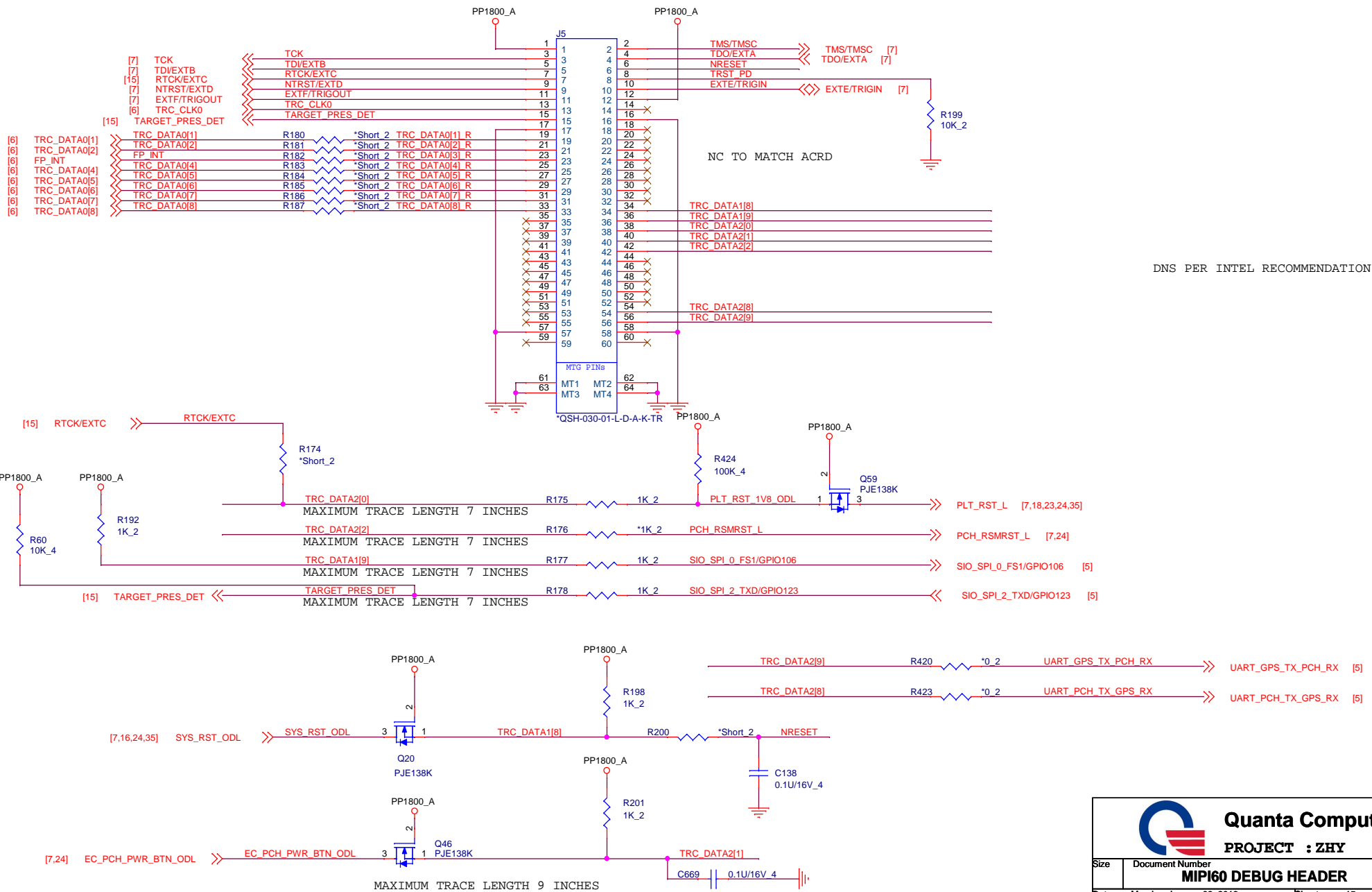


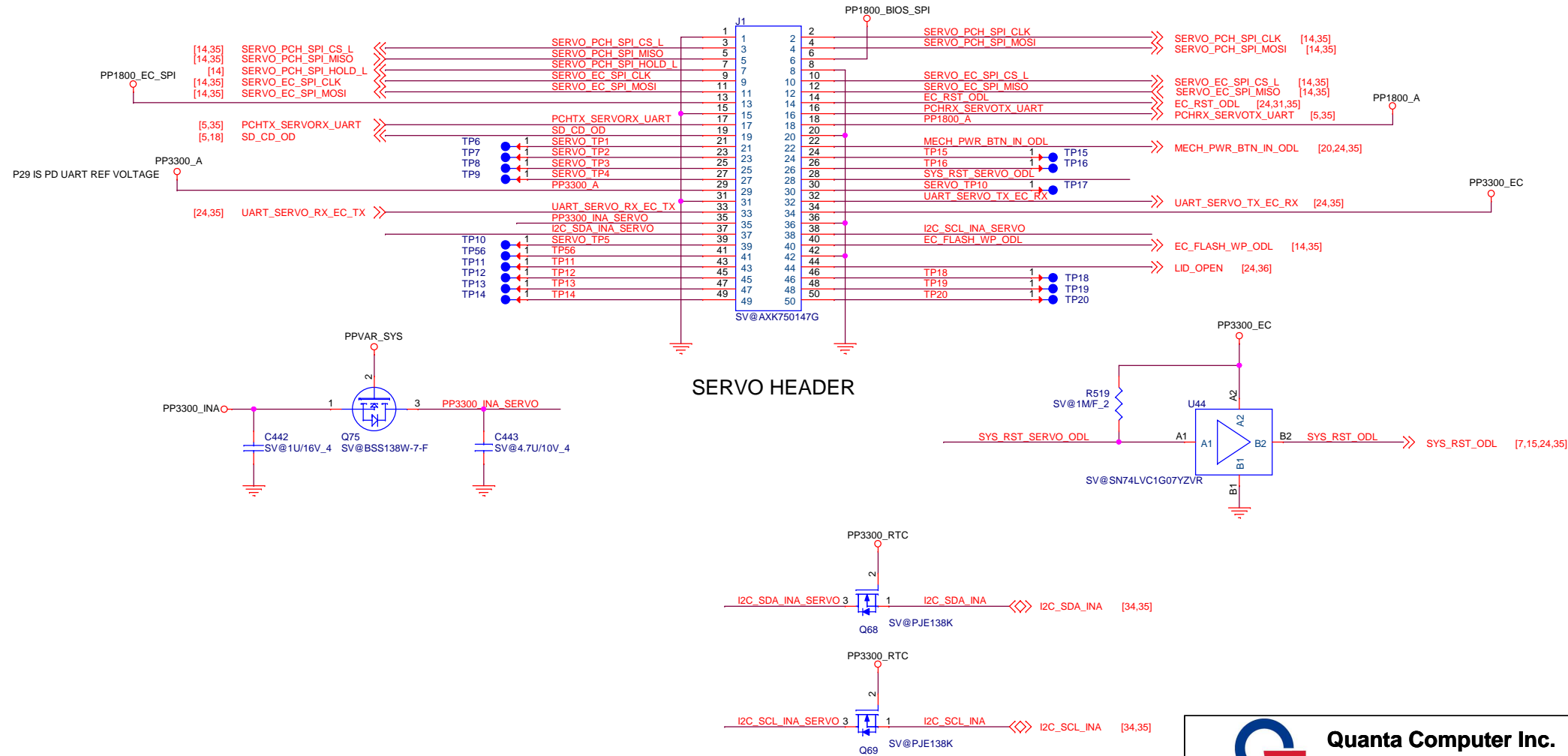
EC SPI FLASH

**Quanta Computer Inc.**

PROJECT : ZHY

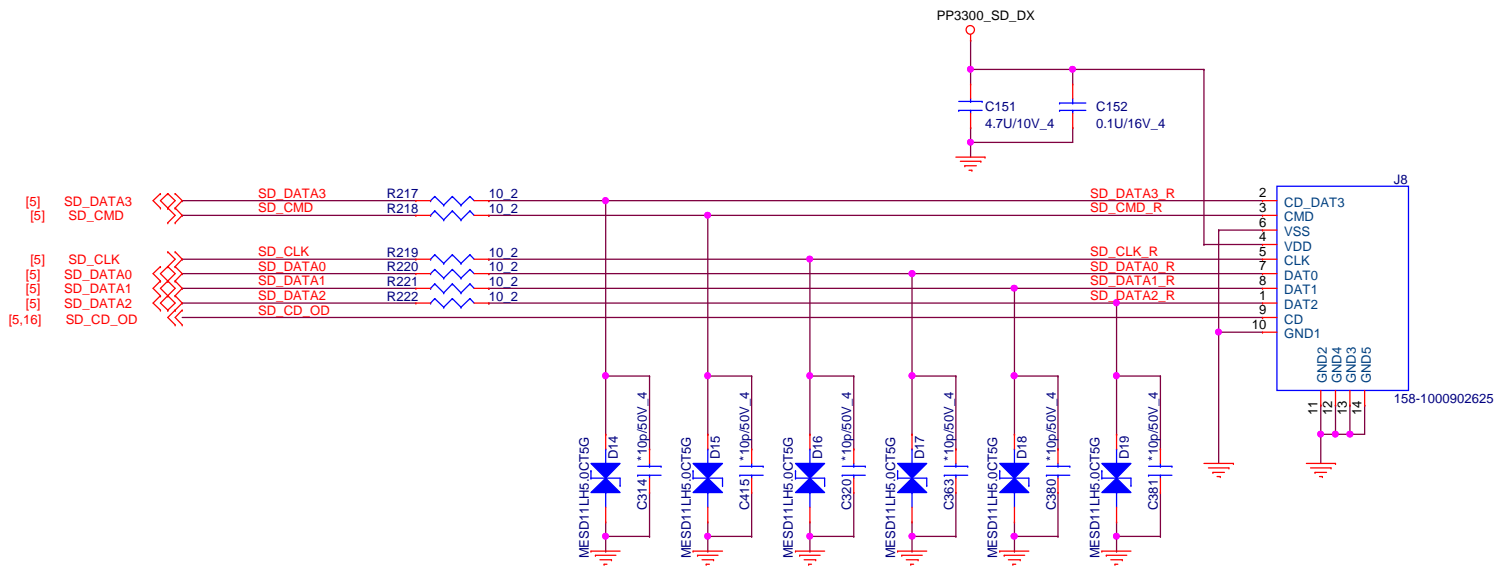
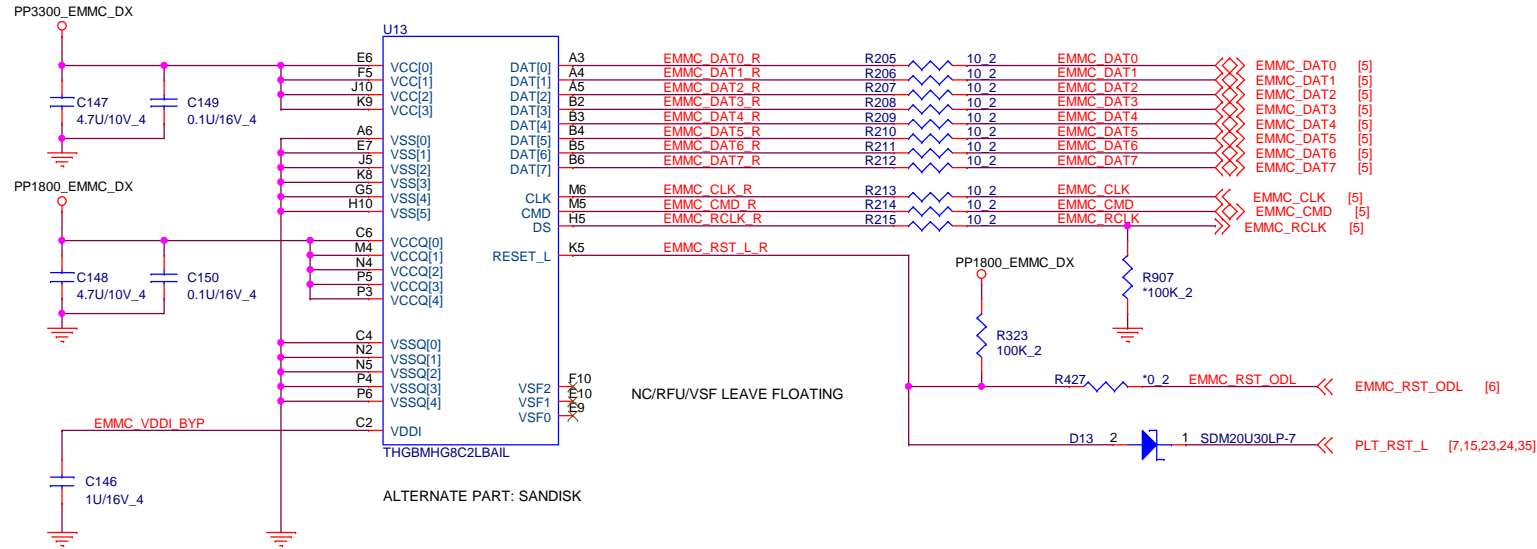
Size	Document Number	Rev 1A
SPI ROM		
Date:	Monday, January 08, 2018	Sheet 14 of 37






32 GB EMMC SD STORAGE

150 UA SLEEP CURRENT

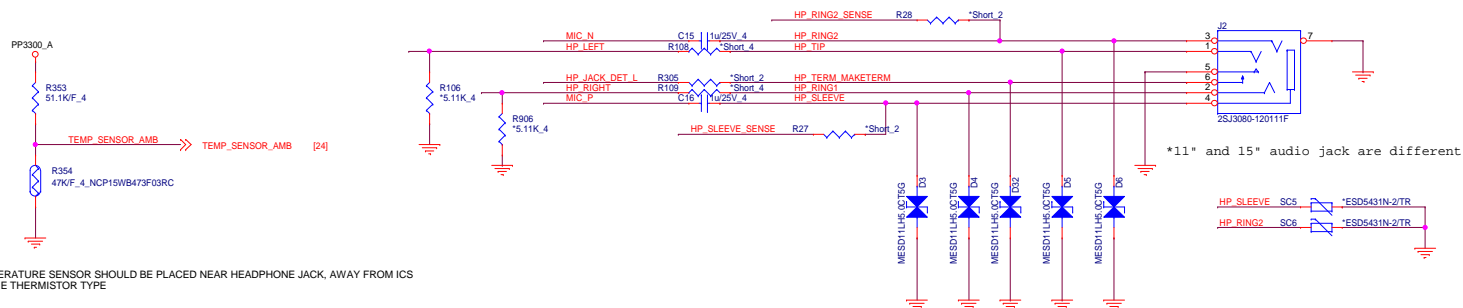
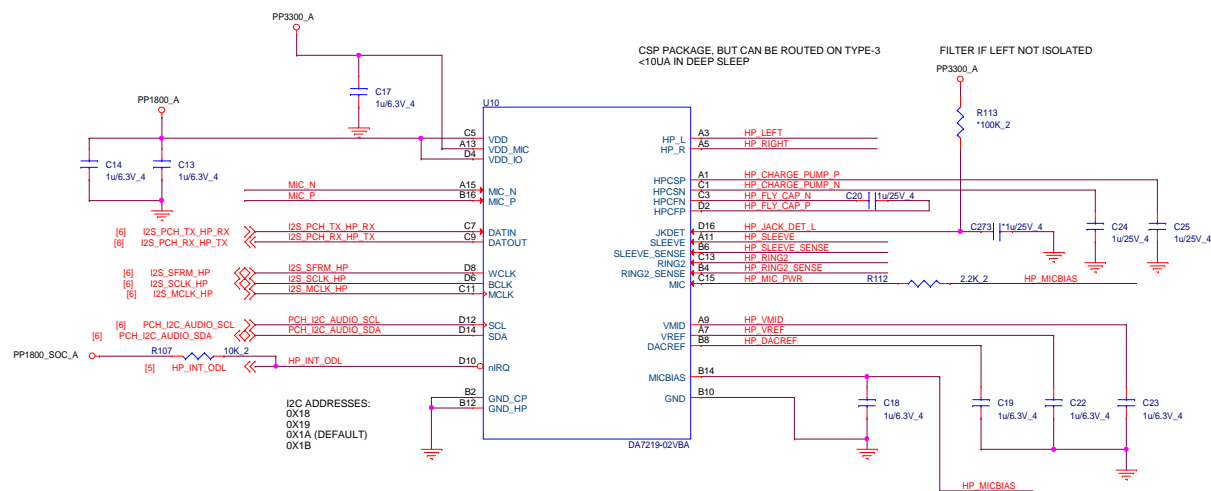
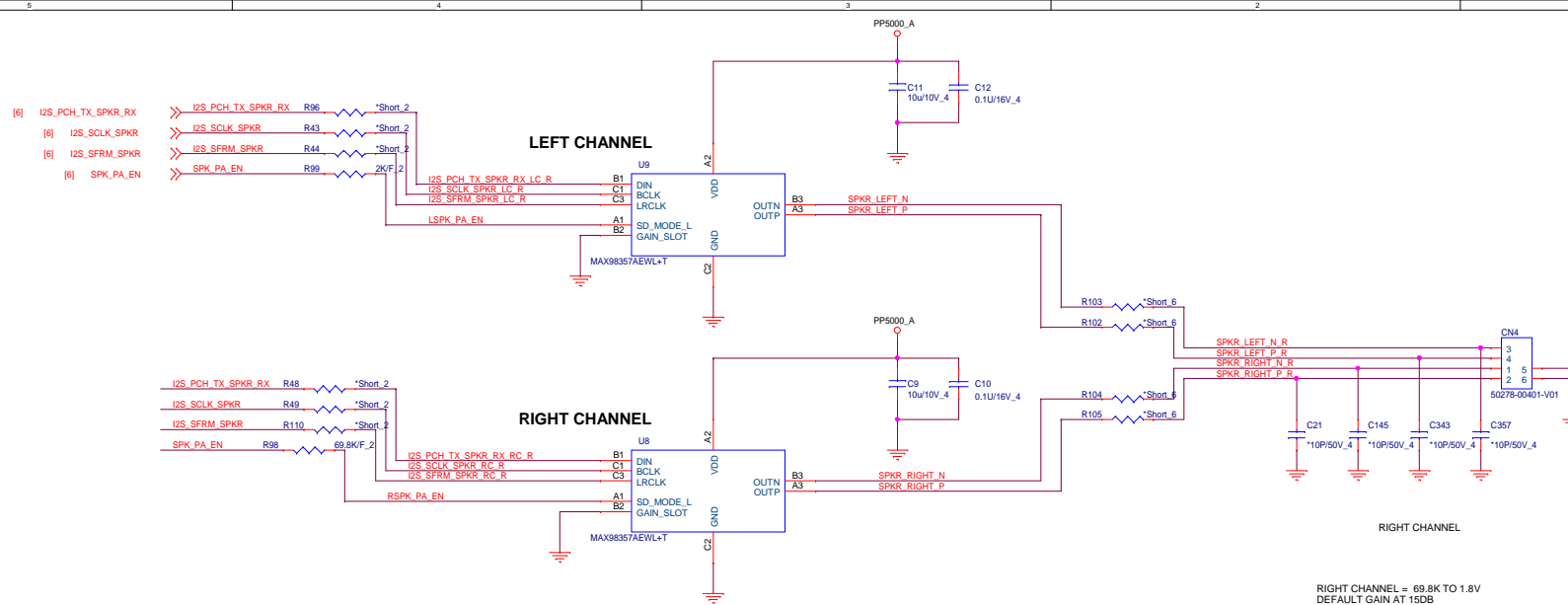




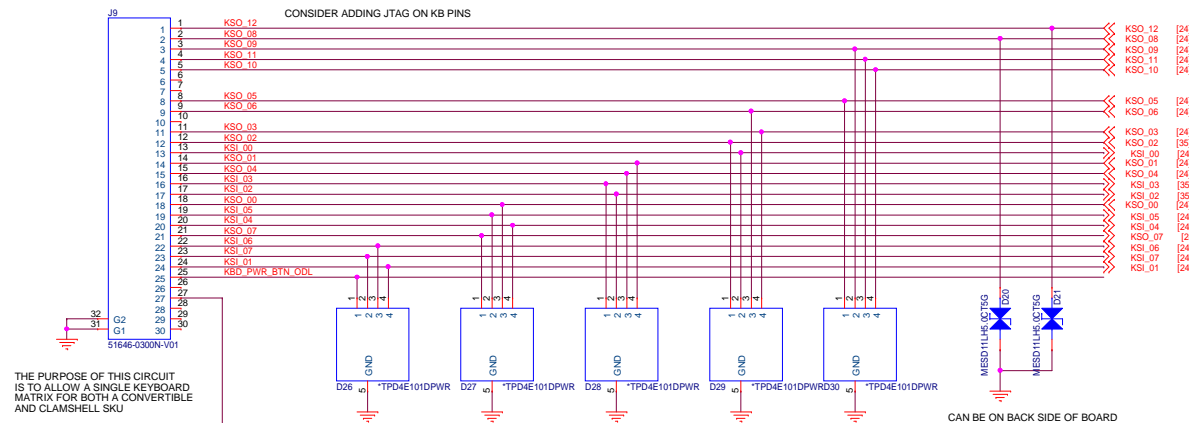
Quanta Computer Inc.

PROJECT : ZHY

Size	Document Number	Rev 1A
eMMC/SD		
Date: Monday, January 08, 2018	Sheet 18 of 37	



BSW REF DESIGN ERRATA MENTIONED
A LEAKAGE PROBLEM INTO THE EC
VIA THE KSO PINS. THE FIX WAS
TO ADD 100K PULLUPS TO 3P3A_EC
ON THE KSO SIGNALS
0C2A COMPATIBLE

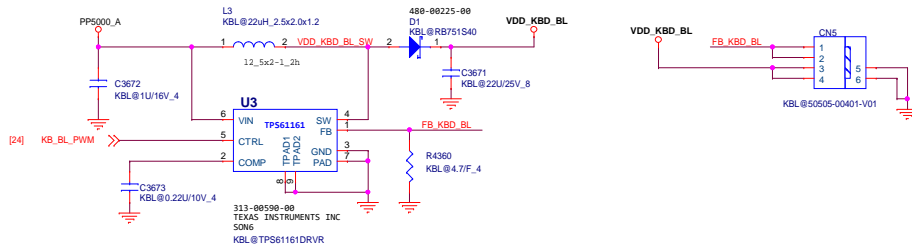


Convertible	R246,R248,SW3 STUFF ; R247,R249 NC
Clamshell	R247,R249 STUFF ; R246,R248,SW3 NC

THE PURPOSE OF THIS CIRCUIT IS TO ALLOW A SINGLE KEYBOARD MATRIX FOR BOTH A CONVERTIBLE AND CLAMSHELL SKU

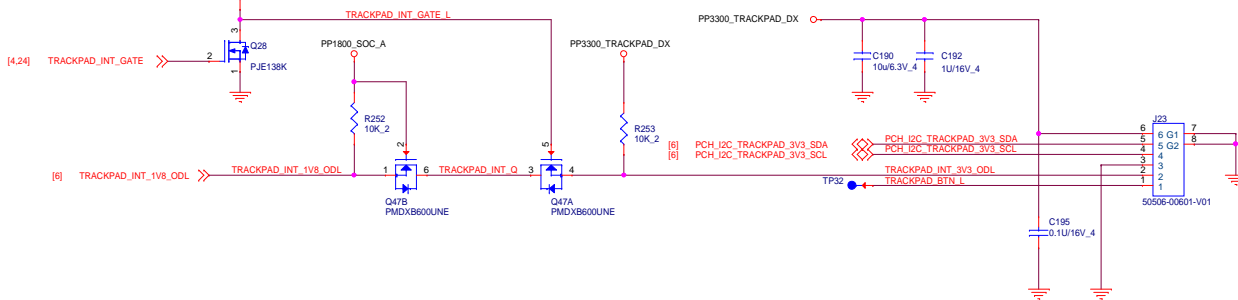
STUFF THESE FOR KEYBOARD POWER BUTTON

KB BACKLIGHT



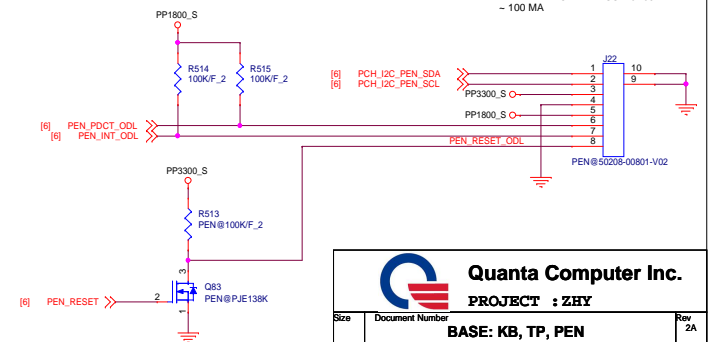
TRACKPAD CONNECTOR

SUBJECT TO CHANGE PER QUANTA REQUEST
TRACKPAD 3.3V TO 1.8V LEAKAGE SHOULD BE TINY

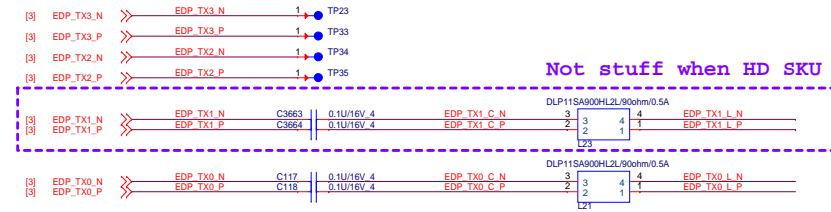


PEN CONNECTOR

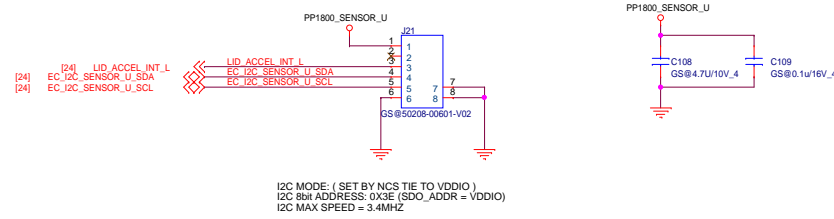
PEN 7-BIT I2C ADDRESS = 0X09
~ 100 MA



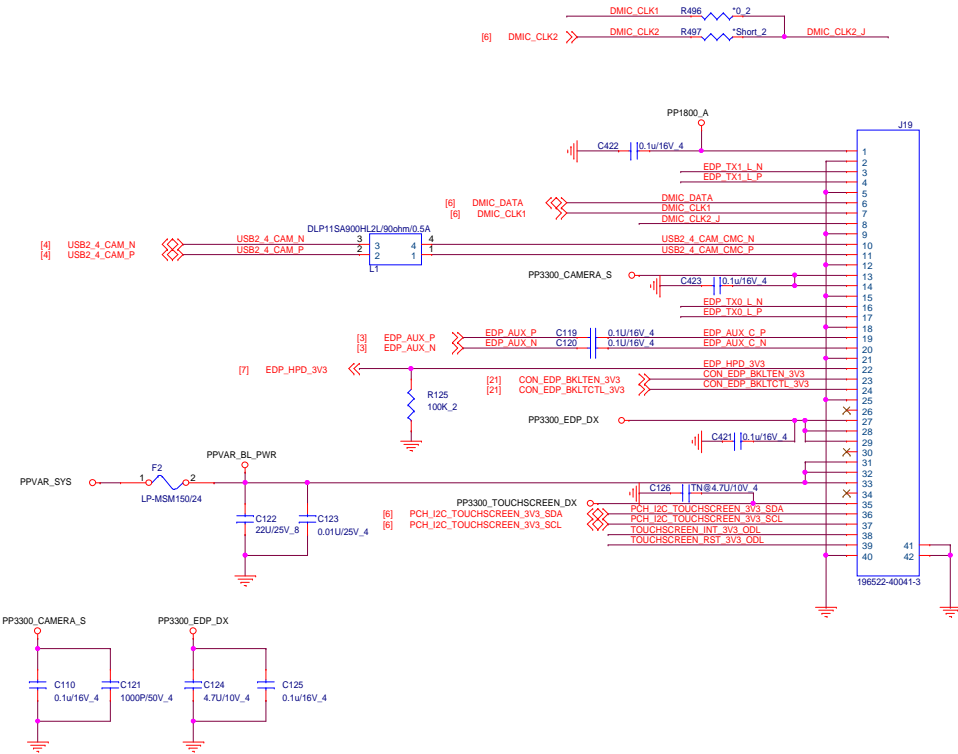
EDP2-EDP3 DOES NOT NEED TO ROUTE TO CONNECTOR



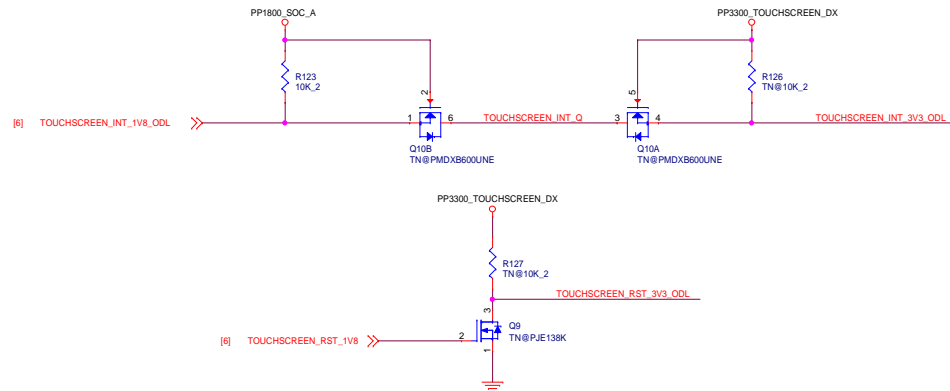
SENSOR BOARD (KX022)



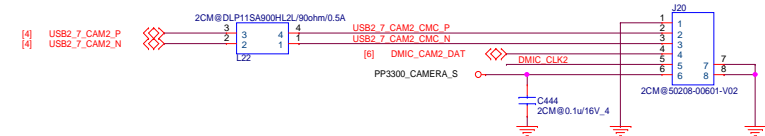
NOTE: SEE WHITE PAPER ON ACCEL PLACEMENT FOR CONVERTIBLES



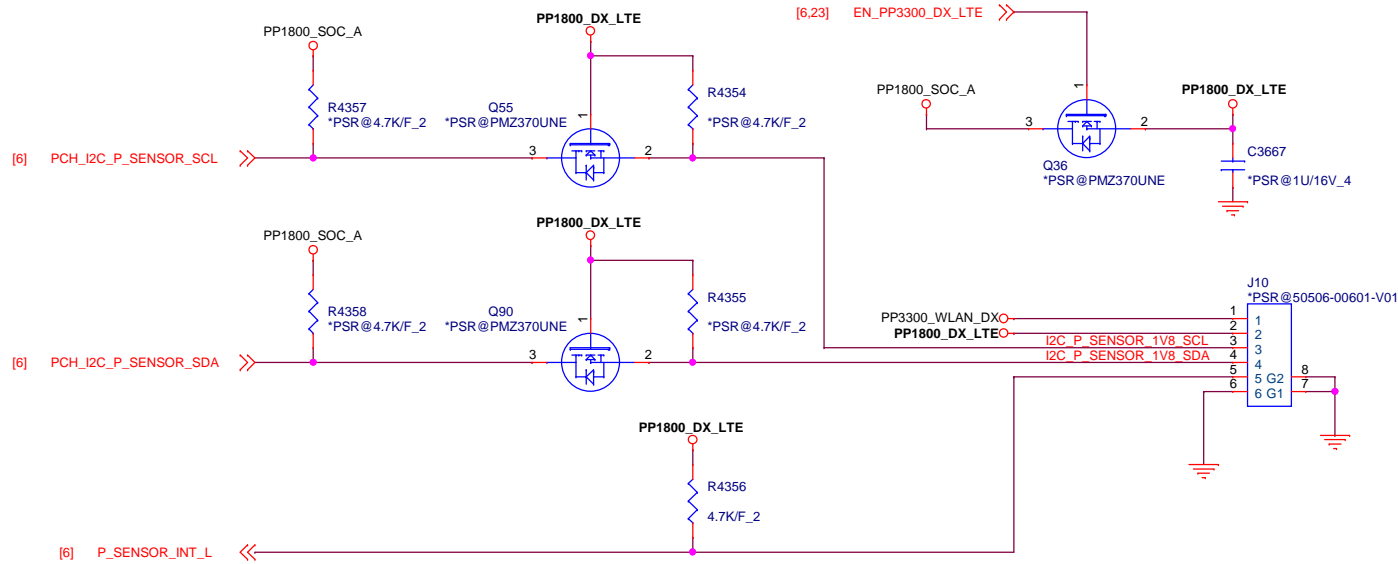
TOUCH SCREEN



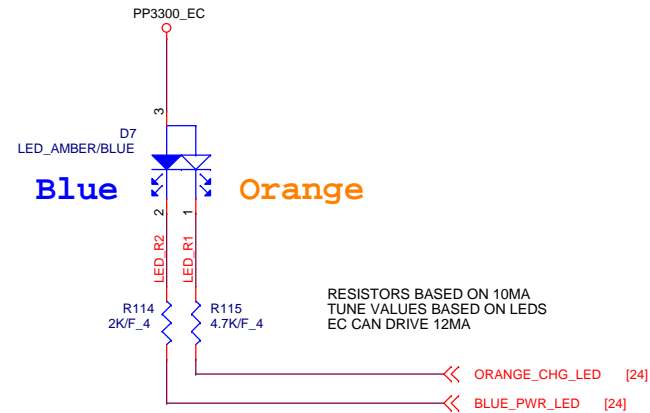
C-PANEL CAMERA



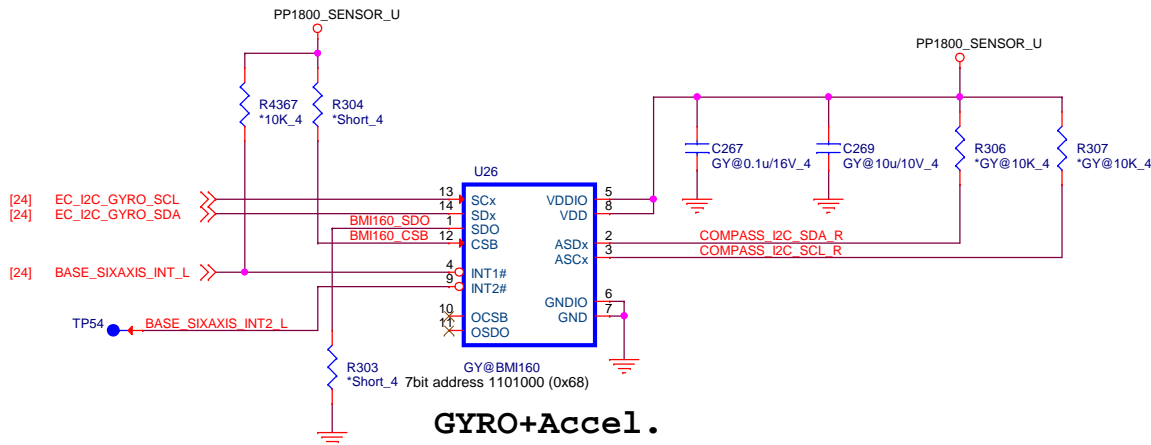
P-SENSOR



CHARGE/BATTERY LED



RESISTORS BASED ON 10MA
TUNE VALUES BASED ON LEDS
EC CAN DRIVE 12MA



GYRO+Accel.



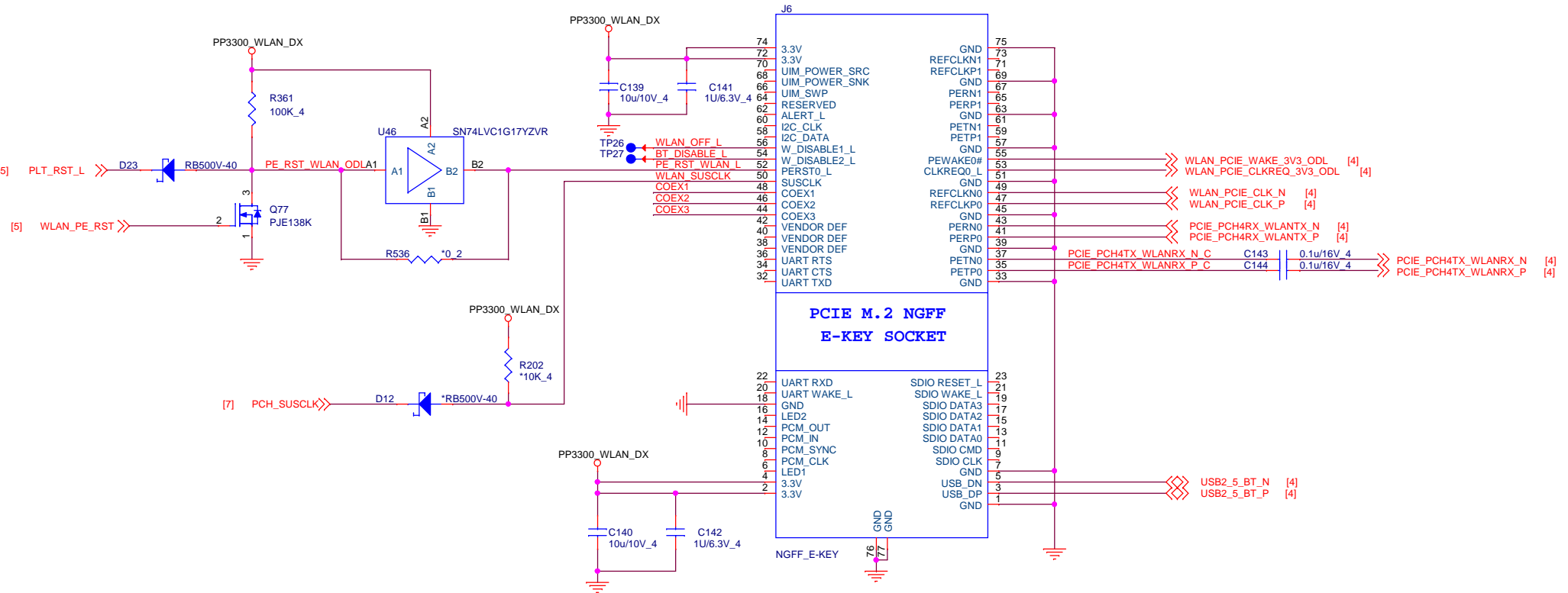
Quanta Computer Inc.

PROJECT : ZHY

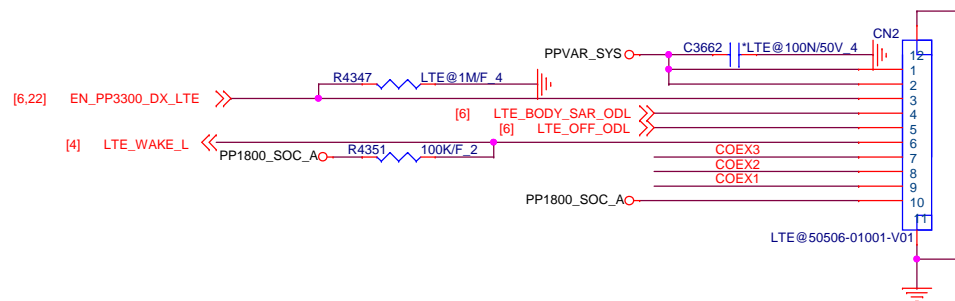
Size	Document Number	Rev
	GYRO, LED, P-SENSOR	3A

Date: Monday, January 08, 2018 Sheet 22 of 37

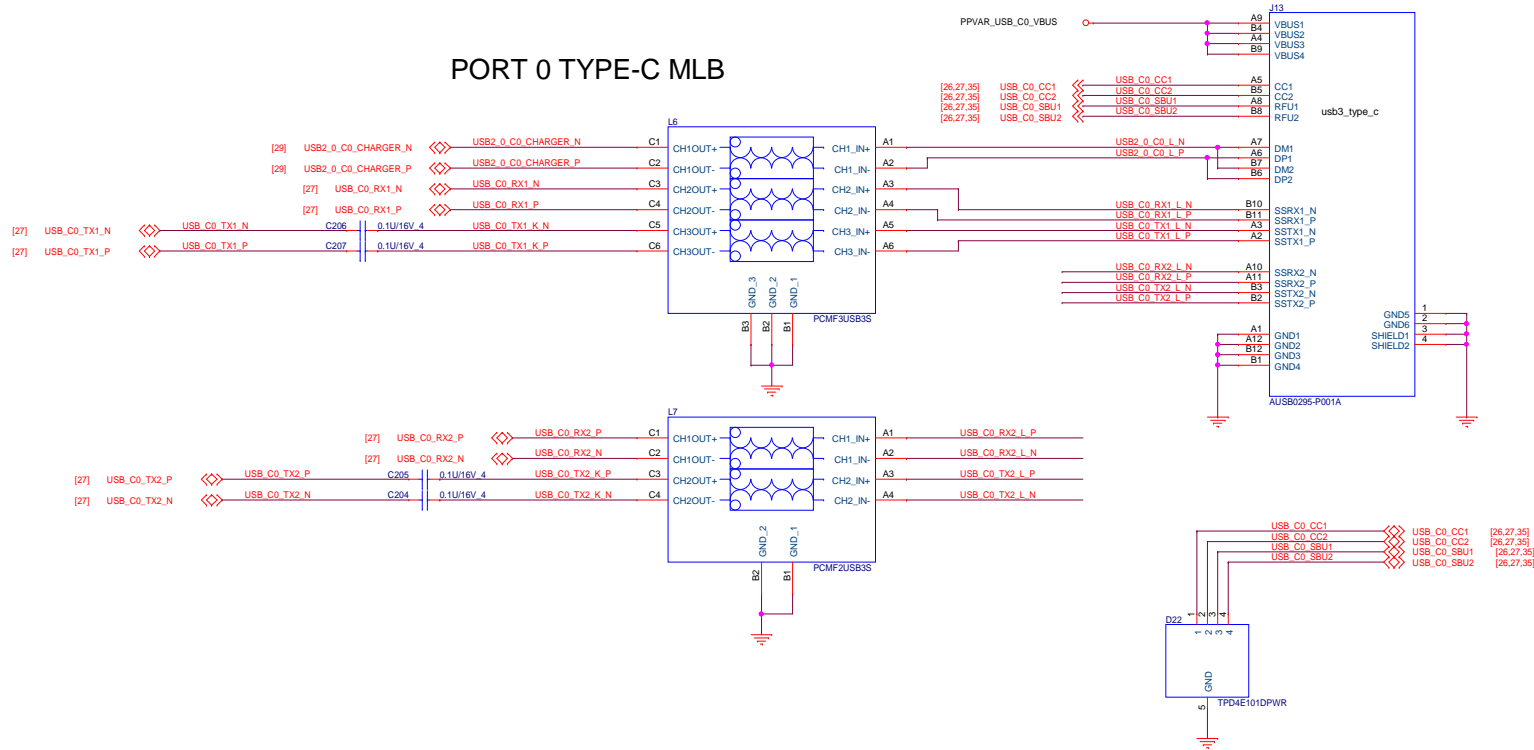
M.2 connector



To LTE/B connector



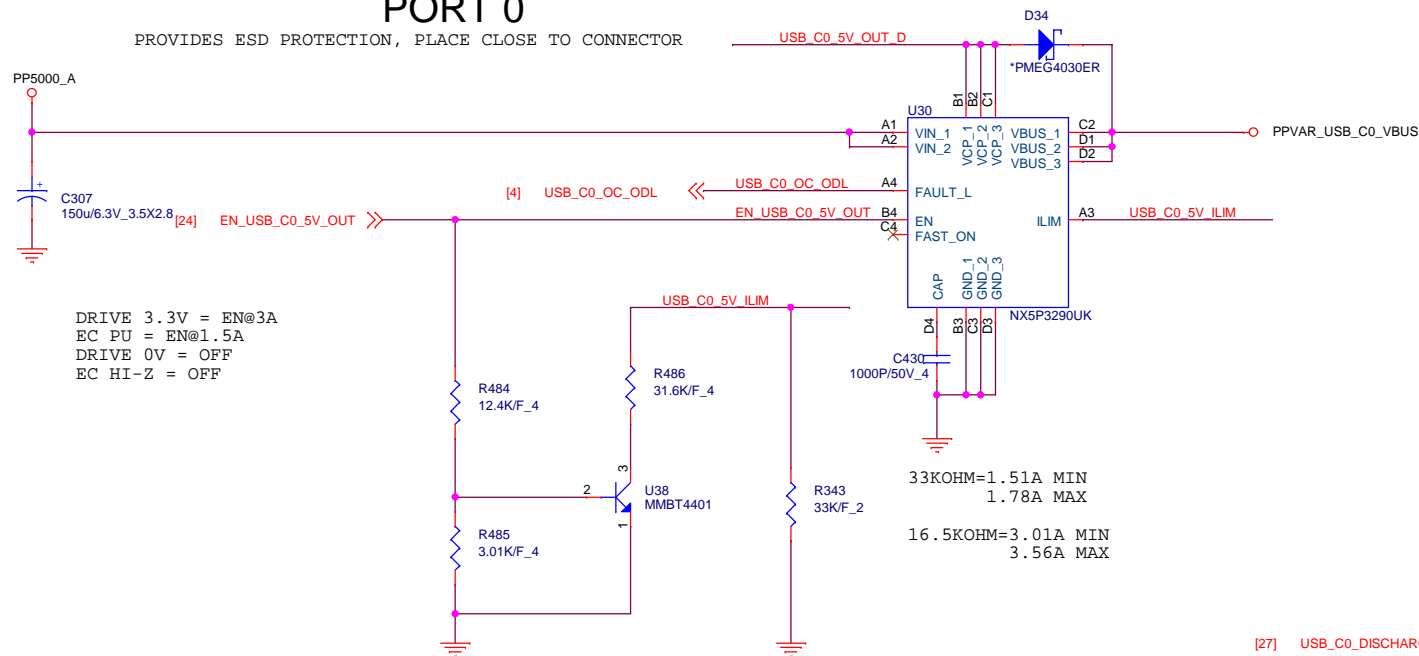
PORT 0 TYPE-C MLB





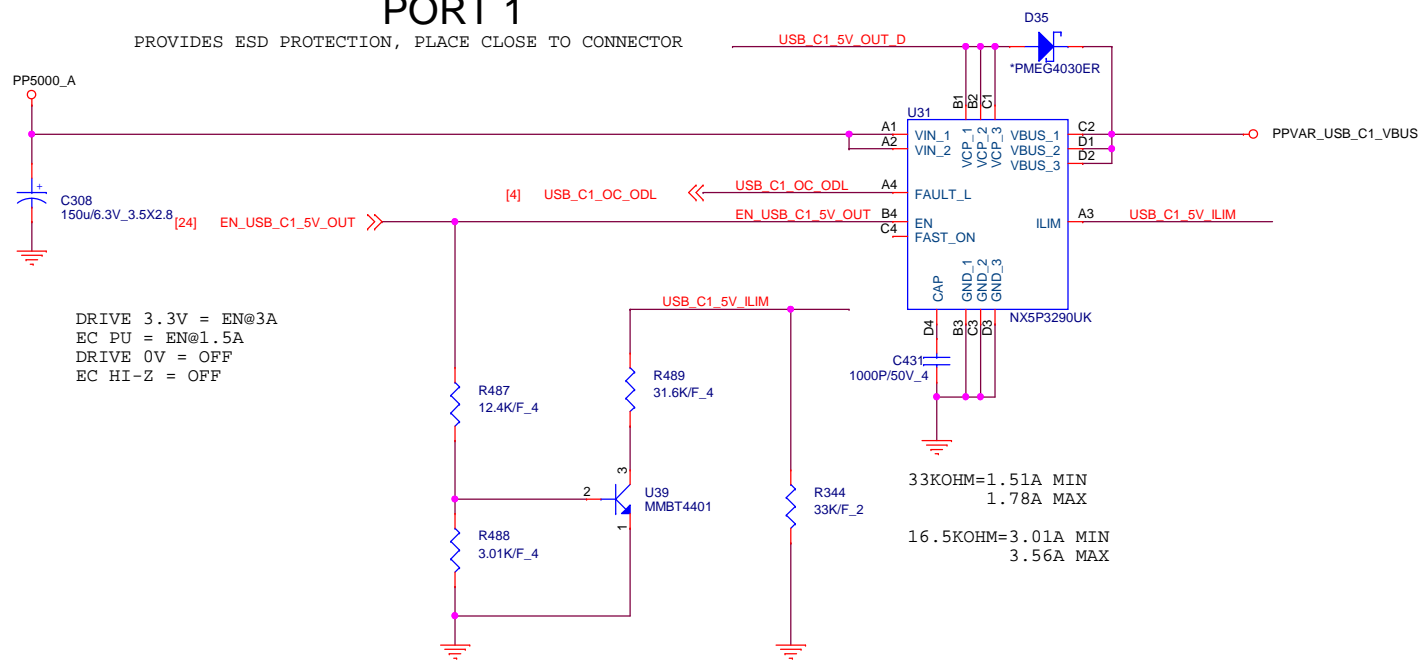
PORT 0

PROVIDES ESD PROTECTION, PLACE CLOSE TO CONNECTOR

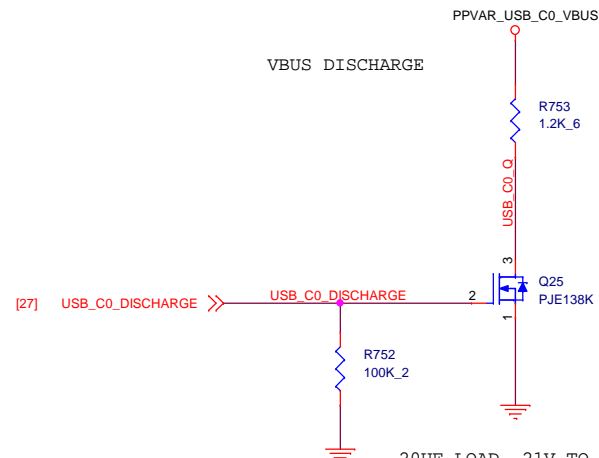


PORT 1

PROVIDES ESD PROTECTION, PLACE CLOSE TO CONNECTOR.



VBUS DISCHARGE



```

20UF LOAD, 21V TO <0.8V IN <80MS
20UF LOAD, 5V TO <0.8V IN <45MS
0402 CAN DISSIPATE 400MW FOR 80MS
0603 CAN DISSIPATE 400MW FOR 500MS
0805 CAN DISSIPATE 400MW FOR 2000MS

```

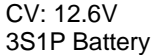
**Quanta Computer Inc.**

PROJECT : ZHY


Size	Document Number	Rev
	USB C 5V OUT	1A
Date:	Monday, January 08, 2018	Sheet 28 of 37

ROHM BUCK-BOOST CHARGER

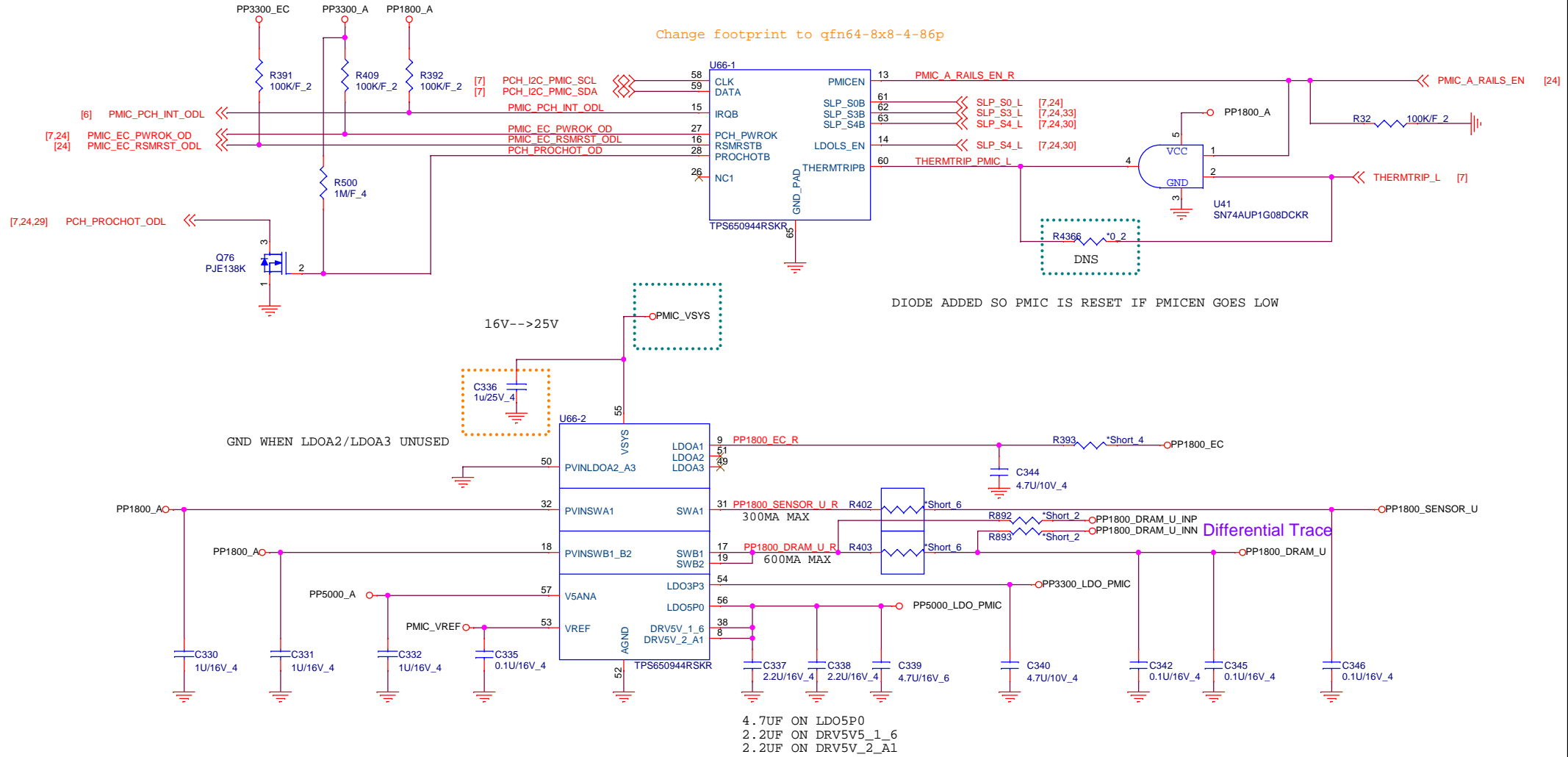
change to 10U/25V_8 from 10U/50V_1206



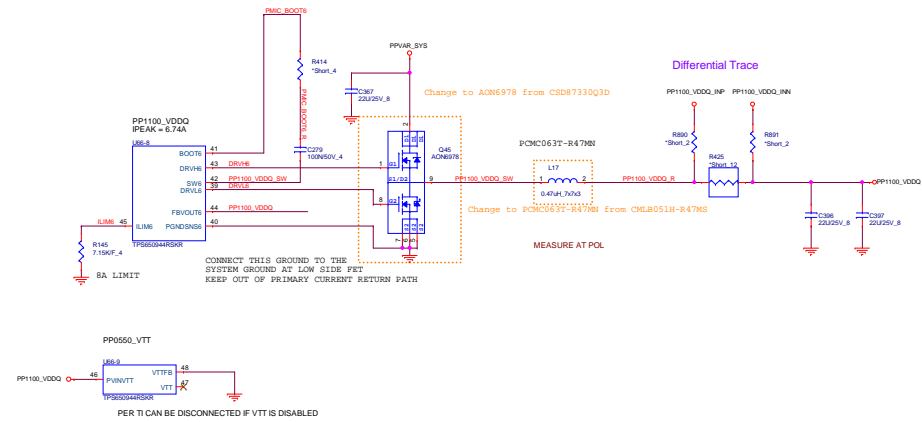
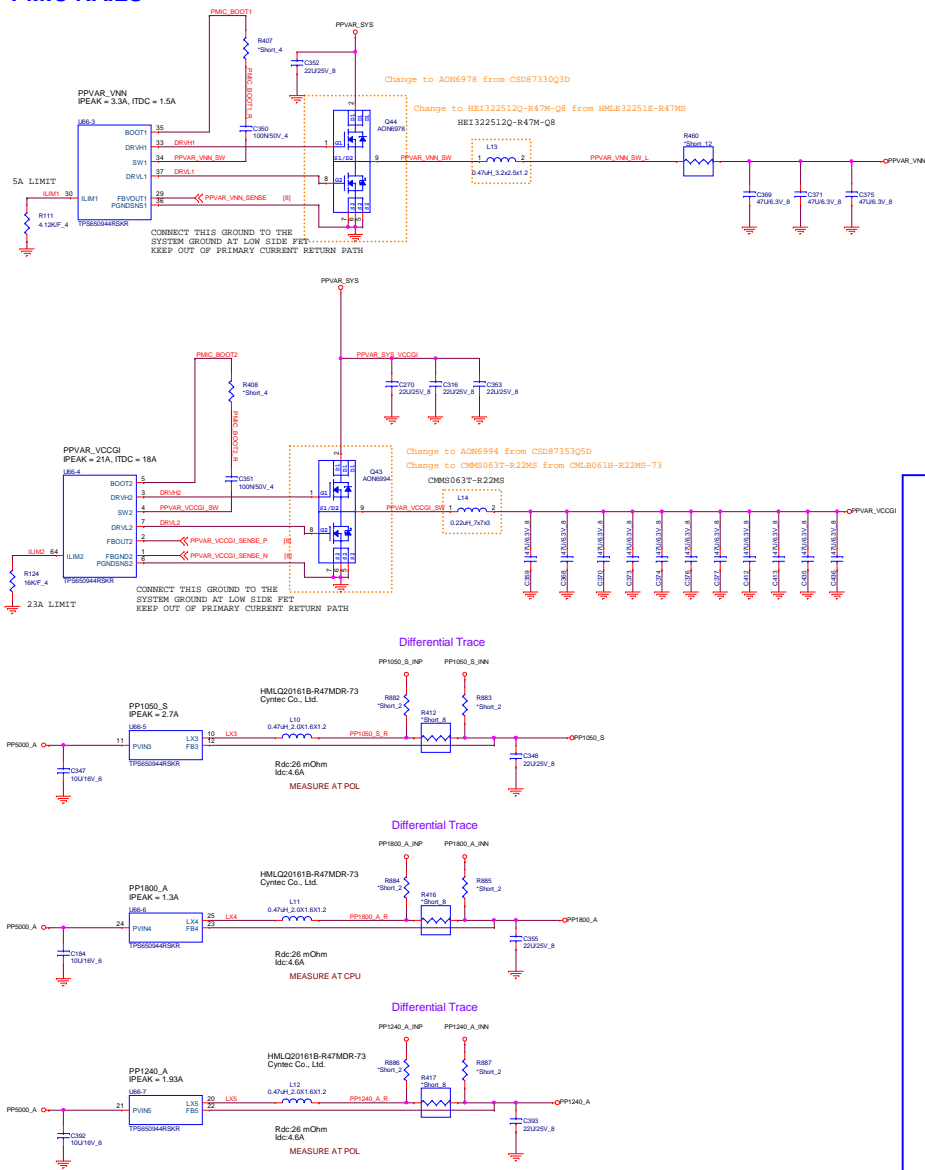
QUANTA CHOOSE PINOUT/CONNECTOR

 Quanta Computer Inc. PROJECT : ZHY	
Size	Document Number POWER - BATTERY CHARGER
Date: Monday, January 08, 2018	Sheet 29 of 37 Rev 3A

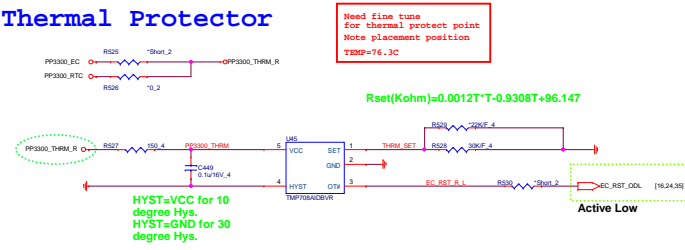
POWER - PMIC LOGIC



POWER - PMIC RAILS



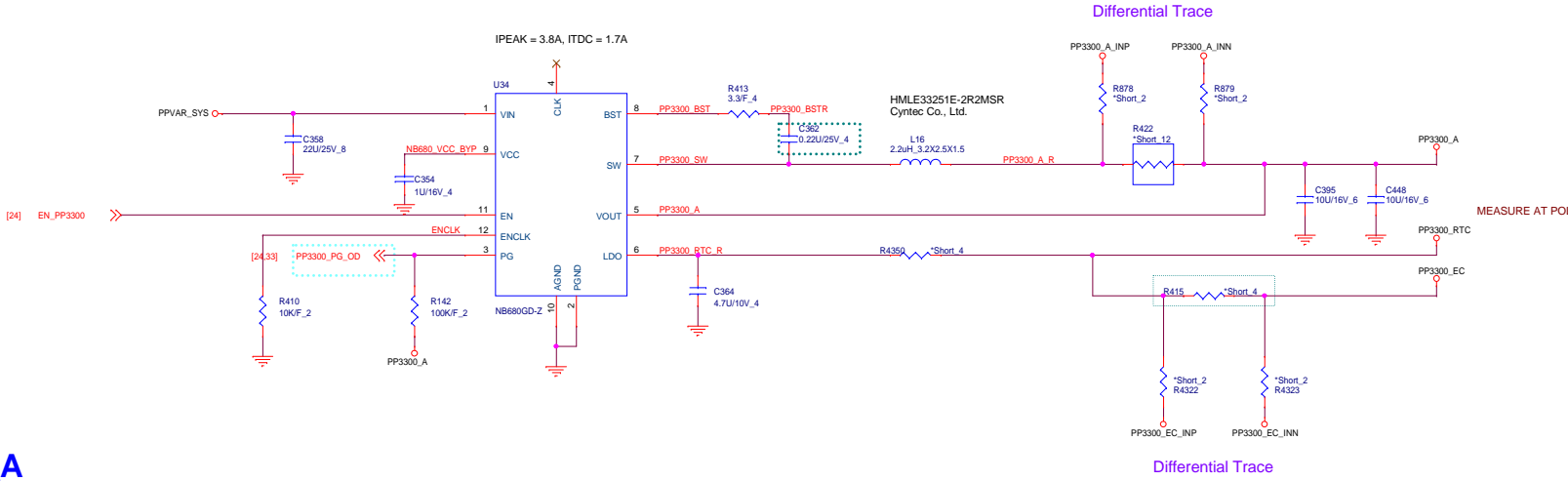
Thermal Protector



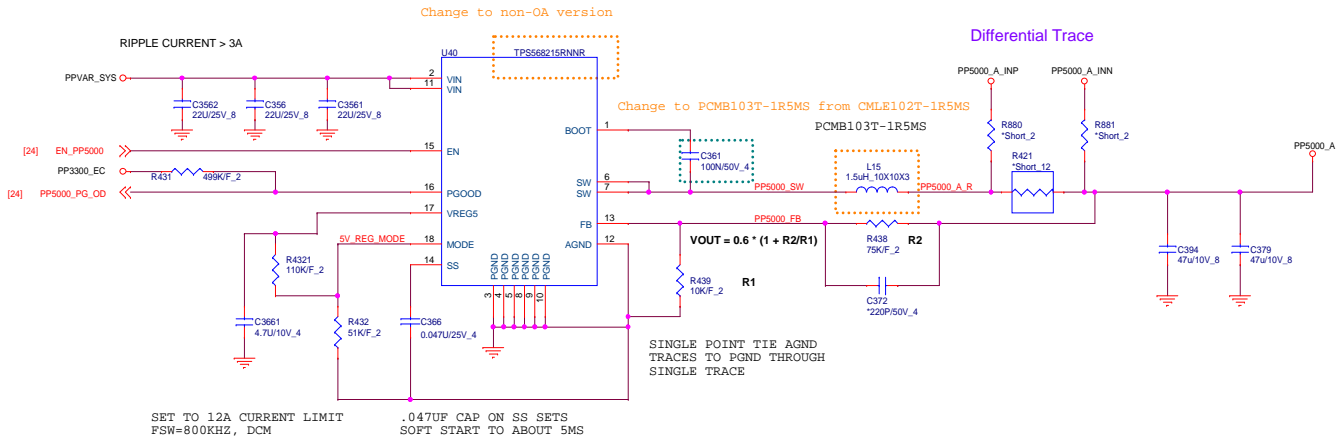
PMIC_VSYS



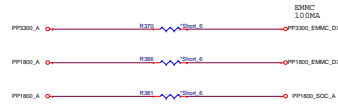
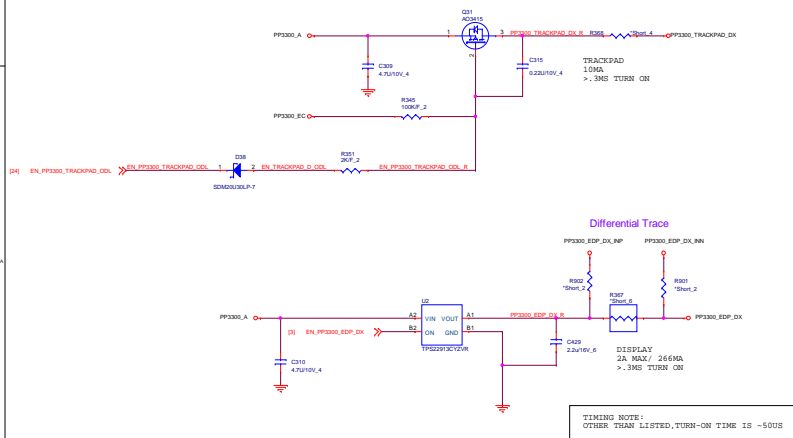
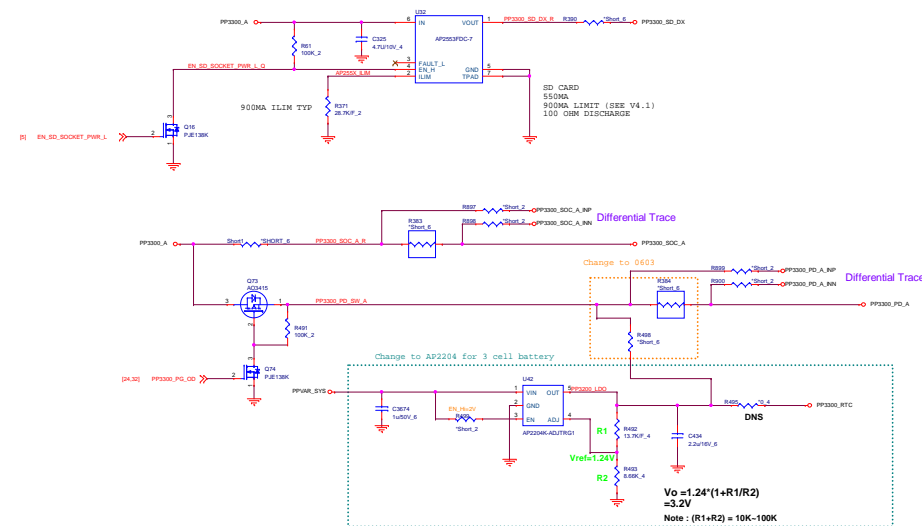
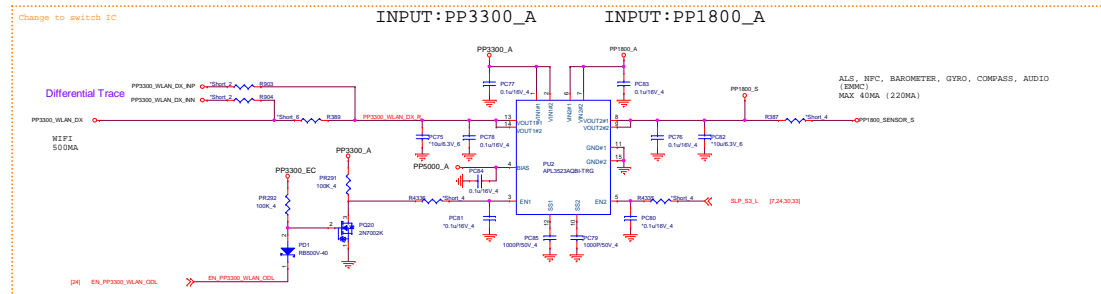
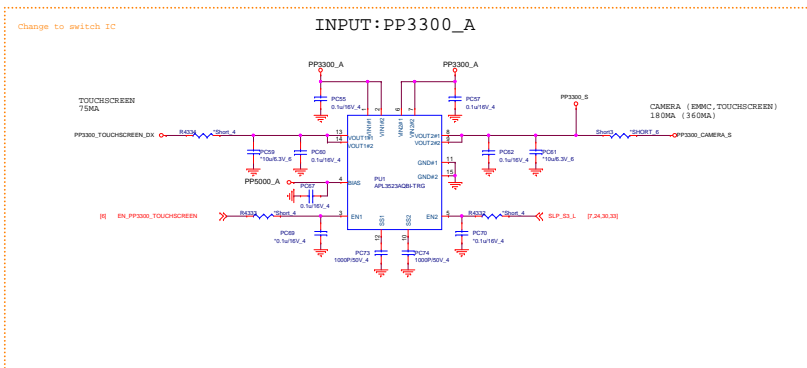
PP3300_A

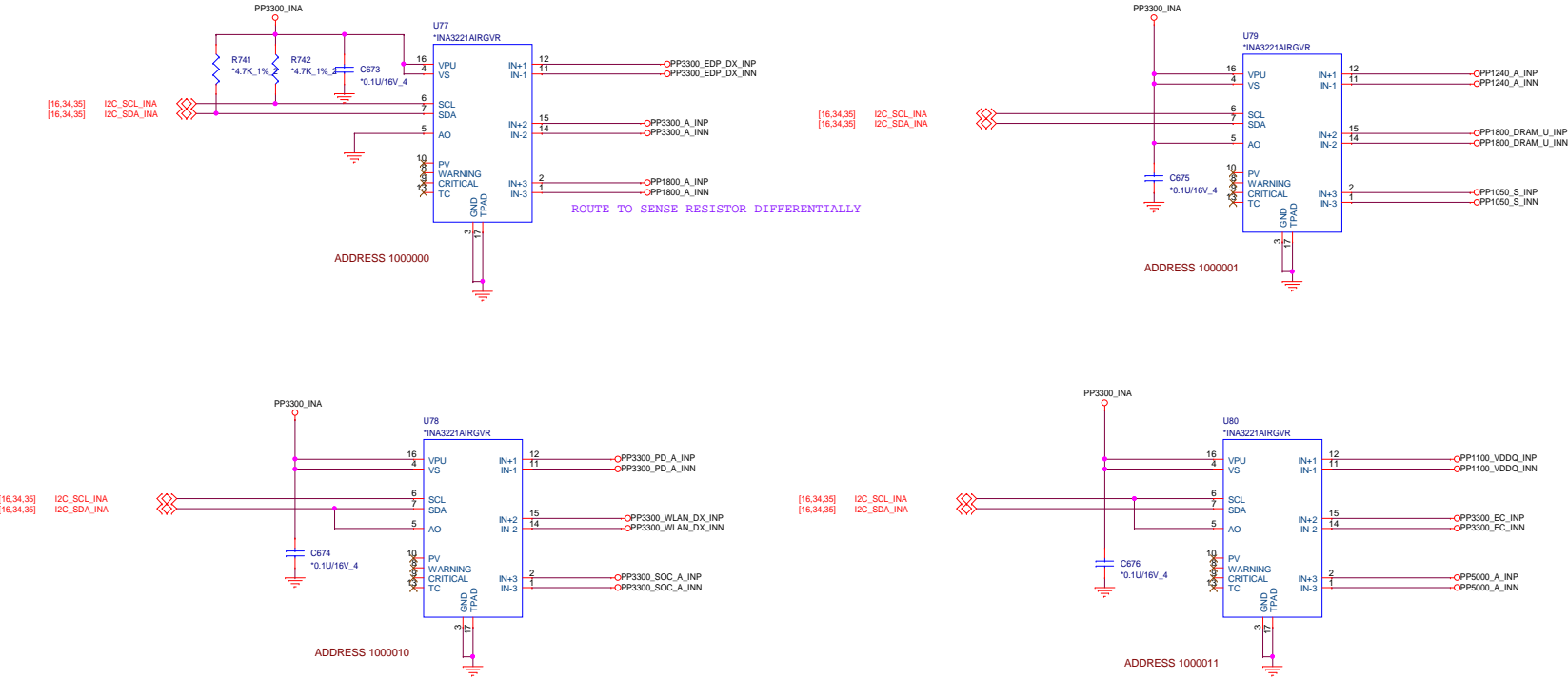


PP5000_A

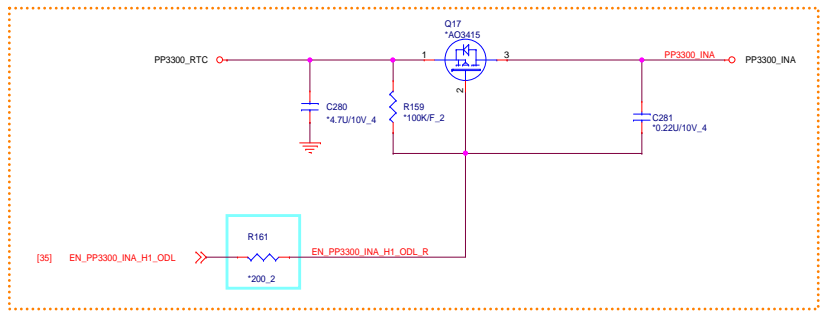


POWER - LOAD SWITCHES

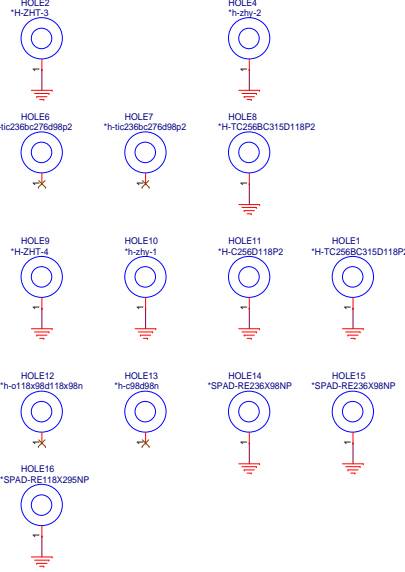




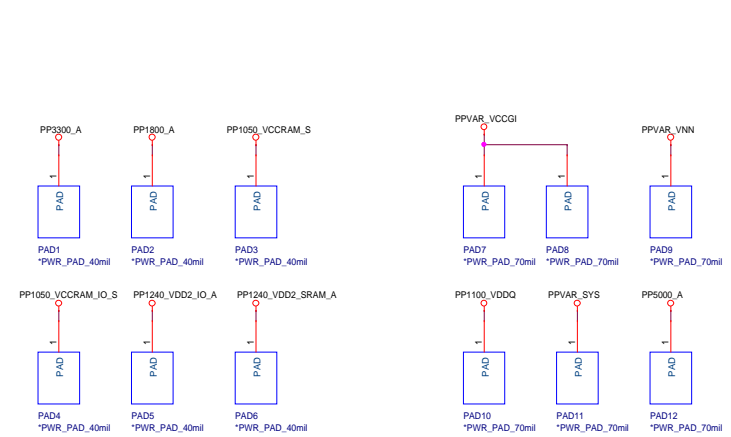
INAs power

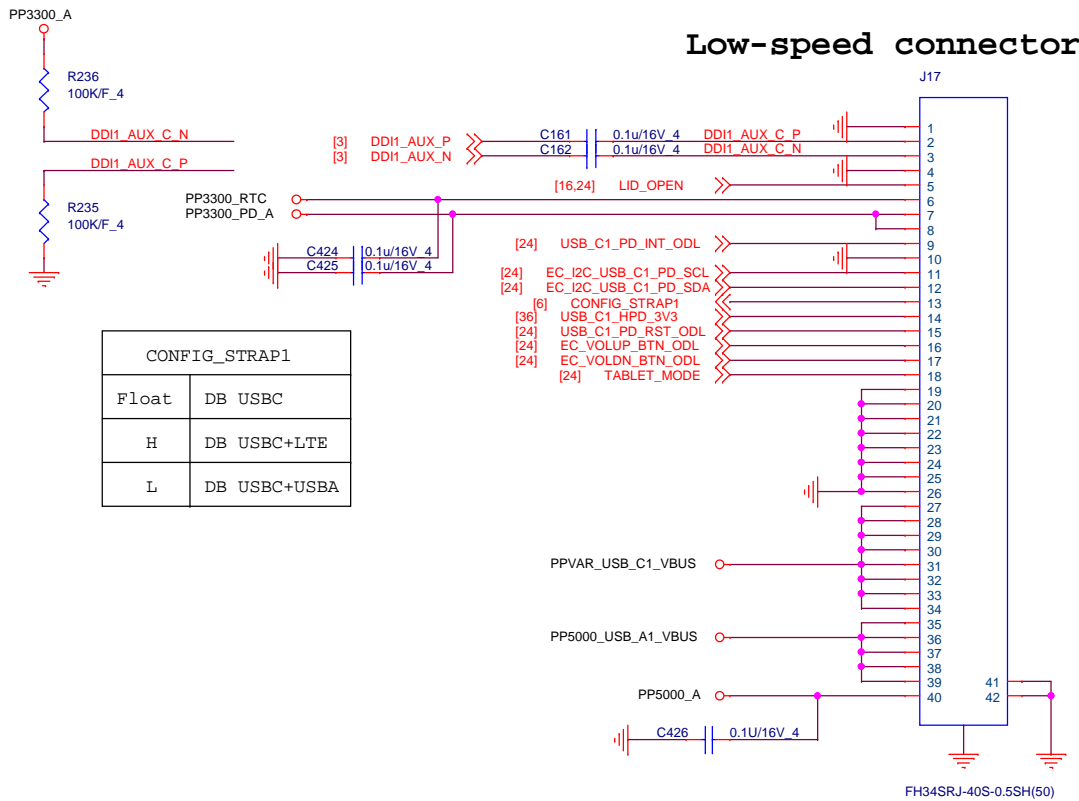


HOLES

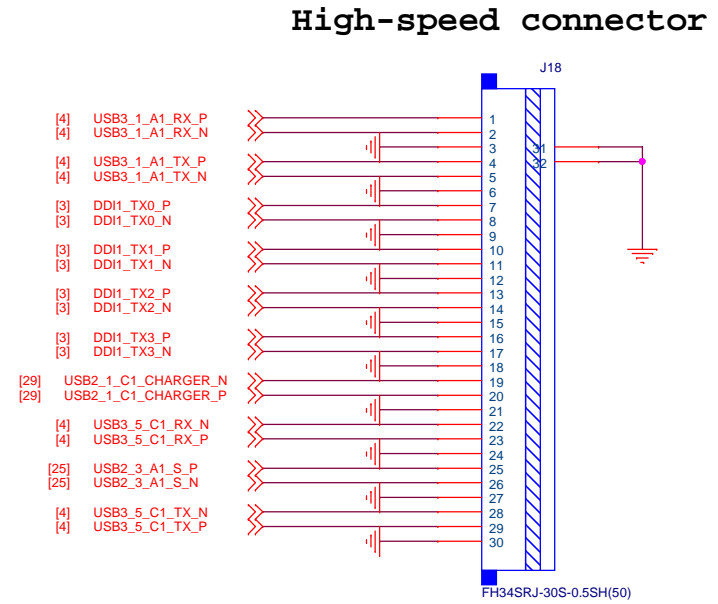
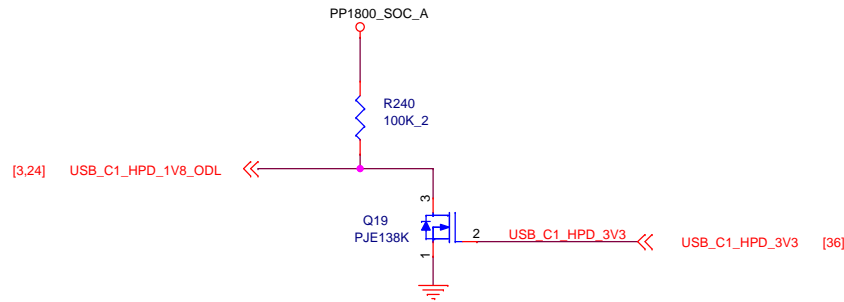


POWER TEST PAD





CONFIG_STRAP1	
Float	DB USBC
H	DB USBC+LTE
L	DB USBC+USBA



Model	Version	CHANGE LIST
ZHY	3A	<div>2017/12/19</div> <div>page 6 Change Q91 to PMDXB600UNE, add R4368 for LTE_EN_PP3300_DX_LTE signal glitch issue</div> <div>page 21 Change R497 to short pad</div> <div>page 35 Change R875,R450,R813 to short pad</div> <div>page 29 Change value to 10u/25V_8 for C296,C297,C410</div> <div>page 30 Change R393,R402,R403 to short pad</div> <div>page 31 Change R460,R412,R416,R417,R425,R530,R4365 to short pad</div> <div>page 32 Change R422,R4350,R415,R421 to short pad</div> <div>page 33 Change R4334,R4333,R4332,R4336,R4335,R383,R381,R367,R499 to short pad</div> <div>Change R498,R384 to short pad</div> <div>page 34 DNS all INAs and INAs power</div> <div>2017/12/20</div> <div>page 29 Add C3675,C3676 (56uF/20V/7343) on PPVAR_SYS for acoustic noise.</div> <div>page 31 Remove PU15,R4364,PR191,PR189,PR188,R4363,PC197,PC195.</div> <div>2017/12/29</div> <div>page 29 Stuff C3676 for all project,stuff C3675 for Astronaut project only</div> <div>2018/1/3</div> <div>page 24 Change R37 to 12.4Kohm for PVT</div>
		<div><div><div><div><div></div><div></div></div><div><div></div><div></div></div></div><div><div></div><div></div></div></div><div>Quanta Computer Inc.</div><div>PROJECT : ZHY</div><div>Doc: <div></div>Document Number</div><div>Rev: <div></div>3A</div><div>CHANGE LIST</div><div>Doc: Monday, January 08, 2018</div><div>Sheet 37 of 37</div></div>